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# electronics





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typical surface-mount assembly shows clearly that this has a rather different appearance than conventional printed-circuit boards. Although they differ externally from their current counterparts. surface-mount devices are internally basically the same, except that they are generally of better quality. Although surface-mount technology is undoubtedly the assembly technique of the future, largely replacing printed-circuit boards within the next decade, it is still in its infancy and dynamic development is likely to continue for some years.

transistors

resistance bridges

resistance decade box . . . . .

This month's cover of a



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# Communicating electronics

Electronic is the world's No. 1 industry. In India, too, it has emerged as the most important one and is on the verge of dwarfing such giants as steel, chemical, automobile, et al.

Such is the impact that electronic has made on the society, if has brought about progressive sophistication and automation; progressive reduction in cost without compromise in quality; and progressive increase in speed and accuracy of operation. Indeed, electronics is progressively leading us to become a high leavel information screen.

At the beginning of this century who could have foreseen such a remarkable metamorphosis in scientific technology. And the developments that have taken place in the lastry decade are a pointer to things to come in the 21st century.

If the progressive and pragmatic policies adopted by our country are any indication, the year 2000 will usher in a century of changes thereby leading us to enjoy convenience and comfort to the full in every aspect of life.

On this cheerful note, elektor, the magazine that communicates electronics, enters the New Year. The elektor group comprises a team of 150 professionally skilled and dedicated people working in Holland as well as in many countries of the world with a common goal—of thowing light on the exciting world of electronics on a continuing basis. The group has made a dynamic success story over last 25 years producing a diverse range of electronic designs for textiles, telecommunication, cars, computers, stereos, ships, and many more.

This tremendous growth stems from: a keen awareness of the need to move with the times; high-reaching ideals; and a strong will towards constant innovation and improvement.

Watchwards these that will carry us to even greater heights in 1986.

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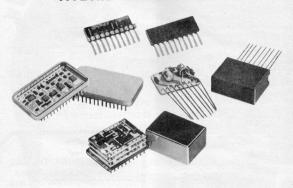
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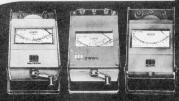
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#### electronics scene

#### Salar Tay Clashad

Television sets used to cost more in Bembay and other parts of Maharashita compared to the rest of the country, a stiff sales tax imposed by the state government had upset the sales well. The government of Maharashita, after a persistent demand from all sections, has decided to reduce the sales tax on electronic goods. All manifacturers of electronic goods, components and peripherals and computer companies have

wetcomed this move.

The sales tax reduction, establishment of three/electronic industrial estates, single window clearance and setting up of a high-level Maharashtra Electronics Development Agency will go a long was considered to the control of the control of

Commerce and industry.
The All India Radio and Electronics
Association, hailing the sales tax cut
as a badly needed boost to the
industry urged the government to
levy a uniform four per cent tax on
electronic goods, irrespective of the
nace of production.

The Maharashtra TV manufacturers' association has expressed the hope that the production of TV sets in the state would increase manifold and assured the government that the concession would be fully passed on to the consumer.

The All India Association of Industries said the sales tax cut would encourage entrepreneurs to set up new units in the state. The government has taken a step in the right direction by this announcement.

TV manufacturers, true to their word seem to have already implemented their decision to pass on the concession to consumers as advertisements have been appearing announcing a reduction in prices.

#### **Export Zones**

The chairman of the Electronics Commission, Dr. M.S. Sanjeevi Rao, has urged the foreign firms to consider relocation of electronic units in India from export processing zones in other countries.

Zotes in other Communities. The wage rates in countries like Hong Kong, Korea and Singapore, where foreign subsidiaries have where foreign subsidiaries have market, and the considerably complained to the wagest complained to the wagest previous finds. It would be profitable previous foreign firms to locate their units in Indian export processing zones, seeminar on "Export processing zones seeminar on "Export processing zones seeminar on "Export processing zones."

as an instrument of export promo-

Development Authority, recently, Exports from the Santa Cruz Electronics Export Processing Zone crossed Rs. 1,000 million last year and by 1990, the exports would touch Rs. 3,500 millions. Currently, 65 units function in SEEPZ, Bombay.

#### Value and output

Electronics will become a forgotten industry unless a demand is created among consumers, according to Mr. S.R. Vijaykar, Secretary to the department of electronics

Research and Development facilities in electronic units should improve not merely to indigenous the imported equipment but also to understand the suitability and adaptability of foreign goods to Indian conditions, Mr. Vijaykar said while delivering the key-note address at a workshop on "Banking requirements of the Flex."

tronics Industry."
The seventh plan target of Rs. 10.000 crores of output set for the electronics industry should be viewed not in terms of value but in terms of obligation, accepted by the planners, the electronics industry would have to become internationally com-

patible both in quality and in price.

Mr. Vijaykar termed the plan allocation of Rs. 4,000 crores to the communication sector as meagre and said at least double the amount was needed to achieve the programme of producing 100,000 micro

computers by 1990.

Dr. C. Rangarajan, Deputy Governor of the Reserve Bank of India, who inaugurated the workshop said the banks should take maximum care in financing electronic products as these faced a high degree of obsole-sence. He desired the development of a new financial norm to ensure that both the promoters and financiers shared the risk equality.

Mr. D.N. Ghosh, Chairman of the State Bank of India said the aim of the workshop was to understand the emerging financial needs of the electronics industry particularly in view of the envisaged massive expansion of investment under the new economic policy.

#### High-Tech MECCA

The Media Laboratory, a new experiment which began early in 1985 at the Massachusetts Institute of Technology, is backed by more than 40 giant corporations with an investment of 40 million dollars for the building and computers alone with

another four million dollars for operating expenses every year.

Media Lab's mission is to explore what computers could be doing ten or 20 years from now. It has diverse and mind-beggling projects that range from talking computers to electronic newspapers, from computer programmes for kids to computers that make music.

The founders of the lab, Nicholar Negroponte, an MIT professor of media technology and Jerome Wiesner, president emeritus of MIT and science adviser to President Kennedy, have convoired a lab different from others. Here researchers buy off-the-shelf equipment and programme them for new uses in creative fields like broad-tasting, publishing, motion pictures.

None of the lab's work is proprietary.
The corporate sponsors can visit the lab for five years and learn anything they want of the various projects.

Marvin Minsky of MIT who is called the dean of artifical intelligence, using computers to try to emulate thuman thought. Seymour Paper is well as the math and education professor of MIT, who developed the computer who developed the computer and all over the world. Negrouped the time the world is set of the math and pioneer in an among the stafflers of the Media Lab.

The Media Lab believes executives will find computers more friendlier in offices of the future. Instead of wrestling with keyboard and enigmatic codes the businessmen will be able to run the computers by talking to it, pointing to ir or even by glancing at it.

One of the research scentists, Richard A. Both wearing special glasses, sits facing as many as 40 delevision pictures, all running simultaneously on a giant screen. Sensors on the glasses track where his eyes are looking and that information goes by cable to a central computer. If his gaze rests on a single picture, the other 39 recede and that project is named Gaze Orchestrated Dynamic Windows project.

The Conversational Desktop is Media Lab's talking computer. It can perform secretarial tasks as making phone calls and reminding the boss of important meetings.

Scientist Bolt has a wrist band with a magnetic sensor that can move an image from one location on a computer screen to another. As Bolt's hand moves through a magnetic field, sensors on the writsband sends signals by cable to the computer, relaying where on the

ecreen he is pointing. His over hand and voice eveteme could possibly anable a busineseman to glance at a screen and have it dienlay a report while talking on the phone

Another section of the Media Lah is busy programming a computer to play a synthesiser following the tempo set by the conductor. When the conductor slowe his baton a conar sensor following his hande tells the computer to have the synthesiser play more slowly The synthesiser can react as quickly as a live musician and plays in nearly perfect synchronisation with a violiniet and a flutiet

Maryin Mineky wante to study what goes on in the mind when someone writee music or listans to it as he believes that understanding how neonle think shout music will ultimately lead to smarter machines

Patrick Purcell an associate profeesor of computer graphics is developing a suit that emits infrared signals to be read by four light sensors connected to a computer which then generates a stick figure on its screen conving every movement. The stick figure could be dressed in any way one likes. NHK. the Japanese counterpart of the BBC has already picked up this technology and created a computeranimated host for a new show on the 21st century.

#### Enter Taletest

A beginning has been made in modernising visual communicatin medium, with the introduction of teletext service by Delhi Doordershan. The service will give information to viewers on finance, railway, airline timings, weather, major events in India and abroad and so on. This service is available from 9 a m to 2 n m and from 3 n m to 10 n m only Criticisms have been made on the ground that this facility is not available to the black and white TV sets and even in colour TV sets, the viewer has to purchase a special decoder. Moreover, the decoders are now imported and one does not know whether all the aspiring colour TV owners have been able to get the decoders. An additional query is on the choice of second channel for telecasting this service. Why not show it on the first channel itself and even TV authorities have no answer for this Viewers in other cifies have been promised that this facility will be extended to them in a phased manner and one hopes that by that time the teething problems will be overcome.

### electronics scene

#### Electron Microscope

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This instrument developed in collaboration with Hitachi Ltd. can be used to examine precisely finished microchins leading to the possibility of even higher intergration of Very Large scale integrated circuits

The high resolution of this new electron microscope has been attained by combining an electric on gun which shoots a very fine electron beam and a method to place samples in a special magnetic lens called high excitation objective lens. The previously most powerful scanning electron microscope, also developed by the Tottori university, provided a resolving power of up to 15 angstroms, thereby yielding magnification by 350,000 times.

#### **New Entrants**

Murugappa Electronics Ltd., a new company has entered the capital market and its project is located at Hebbal industrial area in Mysore district MFI is setting up a project to manufacture 2 350 million metres of 3.81 mm width high quality audio magnetic tapes. It has also registered for manufacturing 23 million audio tanes, using its own production. The cost of the project is Rs 530 lakhs. The company is expected to go into commercial production by April, 1986 and in the first full year of operation it hopes to market about five million cassettes, increasing to about 12 million at the end of the third year of operation.

Computer Point (India) Ltd. which opened the first retail shop for all computer related items under one roof has completed one year and is now entering the capital market. The compnay has retail outlets in Bombay Bangalore and Madras. It has commenced a comuter education service called "Chip club"

A new chain of computer retail choos called "Computer Shock" will com in western Japan has developed the Come up in Rombay and other metropolitan cities following in the foot stens of Computer Point Acparding to Mr. Ashak Samoshwar disaster of Computer Shack the initial project cost was around De 60 lakbe The unit would stock microcomputers in the price range of Re 5 000 to Re 2 lakhe it would also sell packaged and custom-made software computer media, peropherals, computer booke and managings

#### Spookered - by a robot

The world's first snooker-playing robot is expected to be in action in about a year's time. The robot is the brainchild of scientists from Bristol University and London's Imperial College They say that the robot will initially have fairly limited skills at the game but there are plans to develop it further so that it will be canable of taking on top human players such as Steve Davis. The metal maestro of the green baize table will be programmed to learn the rules of the game and to study the position of the balls so that it can work out the best shots. It will then propel itself around the snooker table on wheels to play. The robot will have TV camera eves and the sight will be fed into a computer which will operate as the robot's brain

Spooker is particularly well suited to robotic research because the game requires a high level of co-ordination between hand and eye. Scientists have long been trying to build such skills into robots for industrial applications.

#### Ham Directory

A Directory of Licenced Amateurs in India nonularly known as the Indian Callbook, updated till August 1985. has now been published. The last edition having been published in 1982, this new edition containing over 2000 entries will be found useful by all amateurs (Hams) and Shortwave Listeners (SWLs)

The cover price is Rs. 10/- post free. If supply is required by registered post, Rs. 4/- should be added irrespective of number of copies in the order. The Callbook is available against prepayment only from RADIO, 3 Thiru-Vi-Ka Road, Post Box 725, Madras 600 006, to whom remittances should be sent by Money Order or Bank Draft.

This digital clock makes use of the giant displays published in the August/September 1985 issue of *Elektor India*, it has a face of 720 mm wide by 280 mm high, which makes it readable at distances of up to 100 m. The display alternately shows the time and the ambient temperature.

# JUMBO CLOCK

by A Sevriens

The circuit diagram in Fig. 1 shows that the clock is designed around well-tried ICs. The clocking fraquency is derived from the mains:  $T_i$  is provided with part of the secondary voltage of  $T_i$  and converts this into a suitable rectangular signal. Low-pass filter  $R_i$ - $C_0$  and monostable  $IC_0$  ensure a noise so that the convergence of the signal. This signal is divided by 6 in  $IC_0$ , then by 5 and 10 in  $IC_0$ , and finally by 10 in  $IC_0$ . Then by 5 and 10 in  $IC_0$ , and finally on in  $IC_0$ . When by 6 and 10 in  $IC_0$  then by 6 and 10 in  $IC_0$ 

Circuit IC; functions as a frequency converter signals of 8th IE, 1½ Hs, and 1/80 Hz are applied to its D<sub>4</sub>, D<sub>2</sub> and D<sub>3</sub> input erspectively. When S<sub>5</sub> is in the centre — NORM—position, control inputs A, B, and C of the IG are logic low, and D<sub>3</sub> is then connected to output W. The clock is then supplied with normal minute pulses and piled with normal minute police and place are varieties, S<sub>1</sub> to FAST or SLOW as the case may be.

#### Clockwork

The clockwork is formed by the chain consisting of four-bit synchronous counters  $IC_{11}$  to  $IC_{12}$ . The Q-outputs of these circuits give the counter position in li-bit digital code, where  $Q_{A}$  of  $IC_{11}$  has the lowest value bit. Connections between the outputs and gate  $N_{c}$  are so arranged that when the counter position reads 100 lillillilli (decimal 1439), the three  $IC_{3}$  are reset to 8 This counter position reads 100 minutes with the aid of reset button  $S_{c}$ .

#### Thermometer

Circuit  $IC_{10}$  is the temperature sensor. Its temperature-dependent current causes a voltage drop across  $R_{11}$ , which, after amplification in  $A_1$  is supplied to digitizer  $IC_{14}$ . Provided  $P_1$  and  $P_2$  are adjusted correctly. The Q-outputs of  $IC_{11}$  have logic levels corresponding to the temperature. The digitizer is clocked via agate  $N_2$ .

#### Decoding

The 11-bit digital information as to time and the 8-bit data on temperature are applied to the A and B outputs of multiplexers  $IC_9 \dots IC_{10}$  respectively. The signals at the  $\overline{A}/B$  input of these three ICs determine

whether the time or temperature information is provided to their outputs. The signals at the  $\overline{A}/B$  inputs are derived from the clock oscillator, and arrange for a regular change-over at three-second

The output signals of the multiplexers are simply used as addresses for EPROMs  $IC_{15}$  and  $IC_{15}$ . Two EPROMs provide 18-bit data, and, since four digits are used for the clock, these are divided into four groups of four bits. At each address in the EPROMs now exists the relevant BCD code for controlling each of the four clock digits.

#### Display

How the outputs of the EPROMe control the individual display boards is shown in Fig. 2. Each of the the pipe with a shown in Fig. 2. Each of the the pipe with the expension of the first of the control will be seven segment of the Gooden- see Fig. 4. This decoder converts the BCD codes into control vollages for each individual LED element of the display in accordance with Fig. 3. The BII input of the left-hand display board is connected to the D<sub>c</sub> output of  $U_{cij}^{-1}$  in this is logic low, the display cannot light (so that zeros are not shown in this position). The colon required for time indication is switched off by  $T_2$  when temperature is displayed.

The degree symbol is obtained by making the B-impus of  $\Omega_c$  jogic high, This results in segments h, l, and g of the right-hand distribution of the degree of the de

- they are entirely solid state, which prevents segment failure since the life of LEDs is much longer than, for instance, that of incandescent lamps;
- they do not need intricate reflector constructions:
- if any one LED fails, they remain fully legible by virtue of the speciaal segment construction;
- they are easily arranged in a variety of colours — red, green, blue, yellow, orange;

they work from 24 V with relative high efficiency, which keeps heat dissipation

1.18 elektor india january 1986

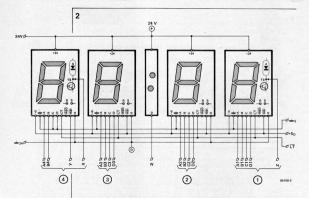


Fig. 2. Connections between the display and control boards.

It may be said that the large number of LEDs required is a disadvantage, but, in our opinion, this is largely negated by the advantages.

The seven-segment display, shown in Figure 4, is based on a type 74LS248 decoder, which has the same features as the weilknown type 74LS47/247, but has in addition internal pull-up resistors and inverted output signals, so that external transistors can be used to cope with the large currents drawn by the segments. The inputs and outputs to the decoder, the read-outs, and the

additional functions are correlated in Figure 3.

All input and output controls have been arranged external to the decoder, so that they can be used in the same way as with normal displays. Wire link R-S serves to interconnect the earths of the +5 V and +24 V surplies.

+24 V supplies.

At the output of the decoder there is a switching stage for each segment that switches the relevant segment on or off.

switches the relevant segment on or off. Each segment consists of four parallel groups of eight or nine LEDs in series with

Fig. 3. The BCD-to-sevensegment decoders on the display boards function according to this table.

3

73.77	er or inputs			1000000	85100-3									
number or function	ΙŦ						RBO/BI				outpu			190
	LI	RBI	D	C	В	Α	1000000	a	b	C	d	6	1	9
0	н	H	L	L	L	L	н	н	н	н	H	н	н	- 1
1	H	X	L	L	L	H	н	L	H	н	L	L	L	- 1
2	H	X	L	L	H	L	н	н	н	L	н	н	1	H
3	H	X	L	L	Н	H	H	н	Н	Н	Н	L	L	+
4	н	X	L	н	L	L	н	L	н	н	L	L	н	F
5	н	X	L	H	L	Н	н	н	L	н	н	L	н	Ė
6	H	X	L	H	H	L	н	H	L	н	н	н	н	-
7	Н	X	L	Н	н	н	H	н	н	Н	L	L	L	ı
- 8 9	н	X	H.	L	L	L	н	н	н	н	н	н	н	+
	H	X	H	L	L	H	H	H	н	H	н	L	H	1
10	H	X	H	L	н	L	н	L	L	L	H	н	L	H
11	Н	X	Н	L	н	Н	н	L	L	н	Н	L	L	H
12	н	X	Н	н	L	L	н	L	н	L	L	L	н	H
13	н	X	H	H	L	Н	H	H	L	L	н	L	н	H
. 14	H	X	H	H	H	L	H	L	L	L	н	н	H	H
15	Н	X	Н	Н	Н	H -	H	L	L	L	L	L	L	L
BI	X	x	X	X	X	X	L	L	L	L	L	L	L	ı
RBI	Н	L	L	L	L	L	L	L	L	L	L	L	L	ı
LT	L	X	X	X	X	X	н	· H	н	н	н	н	н	H

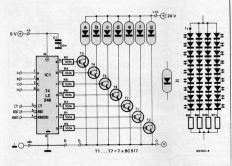


Fig. 4. Circuit diagram of a seven-segment display heard.

Fig. 5. The power supply

a current limiting resistor.

The displays can be powered from a nonstabilized 20...24 V supply. The current drawn per segment varies from 50 mA to 100 mA

Figures Ib and Ic give the diagrams for displays with a "I" and a "" respectively. Both can be used for a 12-hour clock. The "I" display has provision for a lamp test (III", open inputs are considered active, i.e., the display lights. This is in contrast to the seven-segment display which treats inputs that are not connected as logic high, that is, inactive.

As mentioned earlier, read-out boards consisting of several figures may be composed by mounting a number of displays side by side on a flat base. The whole may be protected by translucent red perspex: this also acts as a light filter, which improves the legibility considerably.

As you need a large number of LEDs, shop around for these because many dealers are prepared to allow a quantity discount. Uniformity of brightness of these diodes is not so important for this application, because at the distances for which these displays are intended, differences in brightness do not shown up.

#### Power supplies

Fig. 1 shows that the temperature processing circuits have their separate power supply, +5 A, provided by an additional voltage regulator Type 7805. This arrangement is necessary to prevent the analogue circuits being affected by the digital pulses in the remainder of the unit.

The displays have their own power supply see Fig. 5, which is not regulated. The secondary voltage of  $Tr_2$  was chosen at  $2 \times 18$  V



for red LEDs and good brightness. If green

or yellow LEDs are used, or the displays need not be so bright, a secondary output of 2×15 V at 1.5 A will suffice.

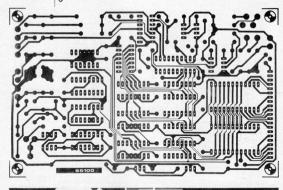
#### Construction and setting up

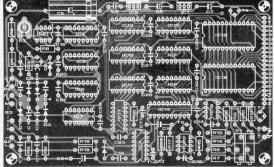
The only problem in the construction is likely to be a wandering of concentration, since there are no fewer than some 2500 soldering joints to be made. It is, therefore, all too easy to make a dry joint.

The clock itself needs no adjustments, but the temperature circuits need to be set up as indicated below.

The temperature sensor is not yet fitted at this stage: in its place, connect a variable power supply (+ to the cathode terminal). The output voltage of the supply should be monitored with a digital voltmeter. As the LM355 provides a voltage of 10 mV/K, the voltage should be set to 2.53 V to simulate a temperature of -20 °C.







Next, connect the digital voltracter across pine 6 and 7 of IG, Set the voltracter to its most sensitive range and adjust  $P_s$  so that the meter reads exactly 0.000 V. Then, set the power supply to 9.230 V, and measure and note the voltage now pertaining across pins 6 and 7 of  $IG_{ij}$ . Finally, connect the voltracter between pin 9 of  $IG_{ij}$ , and earth and adjust  $P_s$  so that the meter reads exactly that the voltage noted before.

Greater accuracy may be obtained by connecting  $P_4$  as shown in dashed lines in Fig. 1, and adjust this preset with the aid of dishes of water at exactly 0 °C and +50 °C. Now, fit  $IC_{18}$  into place.

Finally, connect an analogue voltmeter between +5D and pin 3 of  $IC_{21}$ , and adjust  $P_3$  so that the meter reads about 300 mV. The clock should then operate normally.

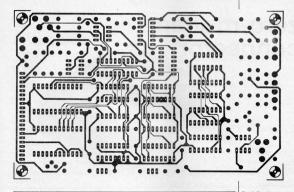


Fig. 6. The (double-sided) printed-circuit board for the control circuits.

#### Parts list (control board)

Resistors:

Re = 82 k

R<sub>7</sub> = 6k8 R<sub>8</sub> = 1k5 R<sub>9</sub> = 3k9

R<sub>10</sub> = 4k7

R11-R20 = 2k2

R<sub>1</sub>;R<sub>2</sub> = 47 k

R<sub>3</sub>;R<sub>14</sub>...R<sub>16</sub> = 10 k R<sub>4</sub>:R<sub>5</sub>:R<sub>17</sub> = 100 k

D<sub>1</sub>...D<sub>4</sub> = 1N4001 D<sub>5</sub>:D<sub>6</sub> = 1N4148 D7; D8 = 1N5401  $T_1 = BC547B$ T2...T4=BS170 or VN10KE or VN1 KM IC1:IC2 = 7805 IC+= 40106 or 74HC14 IC4 = 4018 IC= 74HC390 IC6=4017 or 74HC4017 IC7 = 74HC157 IC<sub>11</sub> . . IC<sub>13</sub> = 74HC163 IC<sub>14</sub> = ADC 0804 IC15;IC16 = 2716 or 27C16 IC17 = 74HC30 IC18 = LM355 IC19:IC20 = CA 3130 ICas = 555 or 7555

Semiconductors:

 $\begin{array}{l} R_{12}\!=\!22\ k\\ R_{13}\!=\!68\ k\\ R_{18}\!=\!100\ \Omega\\ R_{19}\!=\!1\ M\\ P_{1};P_{2}\!=\!2\ k\ multiturn\ preset\\ P_{3}\!=\!50\ k\ preset \end{array}$ 

$$\begin{split} P_{d} &= 10 \text{ k preset}^{*} \\ \text{Capacitors:} \\ \text{C}_{1} &: C_{4}; C_{18} ... C_{23}; \\ \text{C}_{26} ... C_{28} = 22 \text{ n} \\ \text{C}_{6} &= 1000 \text{ g}; 16 \text{ V} \\ \text{C}_{6}; C_{10}; C_{11}; C_{25}; C_{29}; \\ \text{C}_{30} &= 100 \text{ n} \\ \text{C}_{7}; C_{81}; C_{18} &= 10 \text{ g}; 16 \text{ V} \\ \text{C}_{9} &= 47 \text{ n} \\ \text{C}_{12} &= 180 \text{ n} \\ \text{C}_{13} &= 220 \text{ p} \end{split}$$

 $C_7:C_8:C_{16}=10~\mu;16~V$   $C_9=47~n$   $C_{12}=180~n$   $C_{13}=220~p$   $C_{14}:C_{15}=1~n$   $C_{17}=330~p$   $C_{24}=1~\mu;16~V$  $C_{31}=4700~\mu;35~V$  Miscellaneous:  $Tr_1$  = mains transformer with 9 V, 500 mA secondary  $Tr_2$  = mains transformer with 2 × 18 V, 2 A or 2 × 15 V, 1.5 A secondary\*

action
S1=double pole mains
switch
S2= single-pole, press to
make button switch
S3=single-pole change-over
switch with open centre
rest position
heat sink for IC2

F1 = fuse, 1 A, delayed

PCB 85100 \*see text

#### Parts list (colon display)

R<sub>1</sub>;R<sub>2</sub> = 270 Ω 18 LEDs, 5 mm, colour to choice PCB 85413-3

#### Parts list (display board)

NOTE: every component is required four-fold

Resistors: R<sub>1</sub>...R<sub>7</sub> = 100 k R<sub>80</sub>...R<sub>8g</sub>;R<sub>9g</sub>;R<sub>10c</sub>; R<sub>10c</sub>;R<sub>10f</sub>;R<sub>10g</sub>; R<sub>11</sub>...R<sub>11g</sub> = 330 Ω R<sub>30</sub>...R<sub>9d</sub>;R<sub>8f</sub>;R<sub>10d</sub>;R<sub>10d</sub>; R<sub>10e</sub> = 270 Ω

Capacitors: C<sub>1</sub> = 100 n

Semiconductors: T<sub>1</sub>...T<sub>7</sub>= BC517 IC<sub>1</sub>= 74LS248 232 LEDs, 5 mm, colour to choice

PCB 85413-1

zero-modem connector

by J Steeman

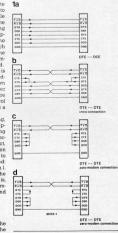
Fig.1 A number of possible connections between two RS232 connectors.

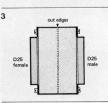
There is probably no other interface that gives so many problems as the RS232. Quite a few connecting wires are needed to ensure the correct links for all possible applications. The reason for this lies in the number of possible handshakes pertaining to the RS232 protocol. With modern equipment, many of these handshakes are no longer strictly necessary, so that a much simpler connection will suffice. At the same time, many idiosyncrasies of various computer manufacturers can be circumvented. The zero-modem connector proposed is based on the idea of reducing the handshakes. Each equipment provides its own handshake while the connector looks after the interconnections of the data lines. There is then of course no longer any control between the individual units, but there is a correct data link

Normally, a cable is needed for connecting, for instance, a DTE (data terminal equipment) to a DCE (data circuit terminating equipment) to a DCE (data circuit terminating equipment) to a DCE (data circuit terminating the cable are then linked direct, i.e., there are no cross-connections. When two computers are inter-connected (DTE to TE), some cross-connections are required: a few examples of these are shown in Fig.1. Fig.1 as the cross-connection used in the present zero-modern connector. It is, therefore, possible to interconnect two computers with the cable shown in Fig.1a and the zero-modern connector.

#### Construction

Two D-25 shells are needed, of which the top is cut away with a metal saw. The





decapitated shells are then glued together at the cut edges as shown in Fig.3. A D.25 female connector is then fitted at one end of the composite shell, and a D-25 connector at the other end. These two connectors should be linked together as shown in Fig.2.

#### Finally

This simple unit makes virtually every RS232 connection possible. Note, however, that it does not relate to connections via a secondary channel as indicated in the V23 protocol. Further, it is assumed that the data formats agree with one another.

Fig.2 Internal wiring diagram of the zero modem connector.

Fig.3 Schematic representation of the composite

The alarm described has been in continual use for over a year at a temperature of around —18°C, which is, of course, quite normal for a deep-freeze alarm. Its function is to indicate an accidental rise in temperature. There are, of course, indicators provided on the deep-freeze unit, but as these are mains-operated they are of not much use in case of mains failure!

The principle of operation is quite simple: a green LED lights as long as the temperature stays within limits defined by the user, while a red LED shows when the temperature has risen above a critical level.

Since operational amplifier IC, is arranged as a differentiator, two possible states ensue: (a) the output voltage is positive as long as the potential at the non-inverting input is higher than that at the inverting input, and (b) the output voltage is negative when the input levels are reversed with respect to those in (a). The voltage at the non-inverting input is derived from potential divider Ro-Ro-Po and is set by the user. The voltage at the inverting input varies with temperature. The sensor is formed by the baseemitter junction of n-p-n transistor T1, which can be almost any type. The value of resistors R3 and P1 depends on the transistor used. The values stated in the circuit diagram pertain to a 2N1711, a threshold temperature of -15 °C, and a supply voltage of +4.5 V.

If more than a visual indication is required, the circuit may be used to control an additional audible alarm. When D<sub>2</sub> lights, transistor T<sub>2</sub> is saturated, so that its collector is nearly at earth potential. This transistor can, therefore, operate a small buzzer or

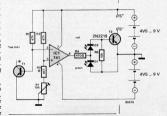
## deep-freeze alarm

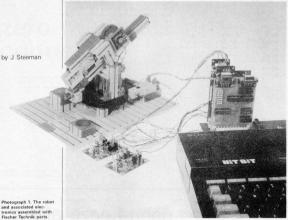
siren, or, indeed, anything else convenient to you. The additional alarm must be connected between S+ and S-.

If you are happy with the LED indication, transistor  $T_2$  may be omitted and resistor  $R_8$  replaced by a wire link.

If only periodic checks are to be carried out, the circuit may be supplied from two 4.5...9 V dry batteries via a spring-loaded push-button switch. Where permanent monitoring is desired, however, it is advisable to use two 6...9 V rechargeable NIGC batteries (without a switch). The current drawn by the buzzer or siren should not exceed 500 m.

C Sadot





# 8-bit I/O bus

Robots are now being used in a variety of industrial processes, but they are very unlike their namesakes appearing in science fiction fantasy. Robotics is by now a generally accepted science, and constitutes an interesting meeting point for applied electronics, mechanics, and computer programming. To control a simple robot, a computer will need input and output channels, so there's another reason to build this expansion for the universal I/O bus.

This circuit, together with the analogue computer input featured in the June 1985 issue of Elektor India opens the

world of control and measurement technology. It enables the measuring and logging of eight analogue and eight digital channels, as well as the control of eight digital outputs. All physical quantities, converted to electric signals by sensors, may be measured by the computer, which checks the results and takes corresponding action, or corrects an occasional system fault via the eight outputs. This procedure is called interactive control.

There is a constant exchange of data between the measured quantities, the computer, and the controlled systems. The software enabling such interactive procedures may be written in BASIC.

#### Applications

Have your computer perform a useful task instead of playing games. Have it guard and control all of your domestic appliances like heating, telephone, aquarium, slide projector, etc. You may also build a computer measuring device to check your loudspeakers or the entire hi-fi installation. These examples, of course, require a certain amount of software to handle the data. The nucleus of such a program is the correct

supply of control data to the input and output channels. BASIC programs with PEEKs and POKEs will be quite adequate for the selection of these channels. The eight outputs are thoroughly buffered and may switch up to 50 V at 0.5 A by means of a ULN 2803.

#### The circuit

1

The CIUCIUT
The ULN 3803 provides an ideal interface between TTL levels and relays, electromagnets, stepper motors, etc. with its high-current Darlington transistor arrays, which allow a peak current of 500 mA. All outputs are of the open collector type and diodes for momentary suppression of inductive surges are fitted internally. The maximum voltage is 50 V, so a variety of relays may be used to increase switching currents and voltages (62 A20 V).

As can be seen in the circuit — Fig.1 —, gates  $N_7 \dots N_{14}$  are driven by two bistable ICs which latch the output data. The bit combination at outputs  $Q_0 \dots Q_7$  is retained

until:

1. the RESET button is pressed. Bus signal NRST goes low, is inverted by  $N_6$ , and clears any programmed data in  $IC_2$  and  $IC_3$  via their CLR inputs.

2. new output data is loaded by a POKE. This involves bus signals SS, R/W, and Φ2. When the board has been selected by SS and R/W is on write, data from the databus is read into the bistables IC₂ and IC₃ during a Φ2 cvcle. 3. the mains supply fails, or the computer is

switched oil.  $C_0$  is selected by gates  $N_1 \dots N_t$ . A low level at the  $G_1$  and  $G_2$  inputs enables data transfer to the bus. This low level exists for the duration of a  $\otimes 2$  cycle (high level), when the board has been selected by the SS (Slot Select) signal and when R/W is in the read mode (i.e. high). The inputs of driver  $IC_0$  have pull-up resistors and accept TTI levels.

#### Construction

If the ready-made PCB, available through our PCB service, is used, the construction of this I/O unit will present no difficulties. Moreover, no adjustments are required. More information on the I/O bus can be found in the June 1985 issue of Elector India.

#### The robot

There are many applications for interactive control, but undoubtedly the most appealing is robot control, for the very reason that one sees what happens. We already have

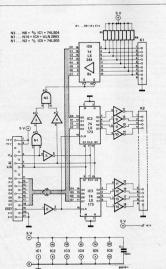


Fig. 1. Another circuit for the universal I/O bus. It provides control of eight input and output

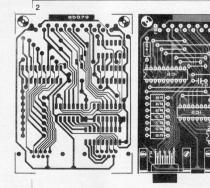


Fig. 2. The board is simply plugged into one of the I/O bus extension connectors.

Parts list

Resistors: R<sub>1...</sub>R<sub>8</sub> = 47 k

Capacitor: C<sub>1</sub> = 100 n

Semiconductors: IC<sub>1</sub> = 74LS04 IC<sub>2</sub>,IC<sub>3</sub> = 74LS173 IC<sub>4</sub> = ULN 2803 IC<sub>5</sub> = 74LS00

IC<sub>6</sub> = 74LS244 Miscellaneous:

21-way DIN 41617 connector, right-angle model K1,K2 = 9-way D-connector, right-angle model PCB 85079 the controlling elements such as computer, I/O bus, analogue input, and the present circuit. Only the robot is missing. A prototype robot shown in the photograph was built from Fischer Technik parts. Its movements may be imitted, but it is eminently suitable local parts may also be used to build other devices like a late, elevator, antenna rotor, sorting machine, pantograph, or a solar cell tracking system.

The robot's arm (Fig. 3) may be moved up

and down through an angle of thirty degrees. At the same time, it can revolve around its own axis, thus creating a coneshaped working area: Fig. 4.

snaped working area; rig. 4.
The robot is able to move small metal objects to and from different locations within its working area, by means of a small electro-magnet at the end of its arm. Two motors move the arm: one takes care of the movement in the vertical plane, while the other revolves the arm around its axis. A set of gears enables the spindle of noters.

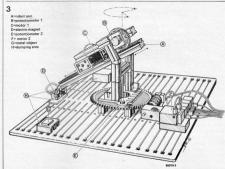


Fig. 3. The most important parts: they enable the robot to move a coin from one place to another.

tiometer 1 to turn in line with the first motor, while potentiometer 2 is driven direct by the

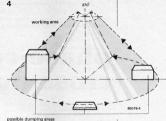
notentiometers are connected hetween the +8 volts supply and ground so that the voltages at their wipers vary hetween these notentials according to the position of the arm. These two voltages may he read by the computer via ite analogue input and thus provide information about the position of the arm. A level of 5 volts is translated to the binary value 1111 1111 = 255 by the A/D converter. Thus, 2.5 volts equals decimal 198: 1.78 volte equals decimal 64: etc. These values are subsequently read by the computer with a PEEK command for comparison with the position to which the arm is to be moved

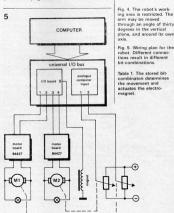
arm is to be moved.

Moor control is effected by four output channels of the present I/O unit. Outputs 1 and 2 move the arm up and down, whereas outputs 3 and 4 move it in the horizontal plane. As far as their power consumption is concerned, the motors could be connected direct to the outputs, but this would preclude the essablishing of revening their directions of the control of the connected circuit such as the one published in the August/September 1984 issue of Elektor India page 1884 will be needed.

2012a2 page 2000. Min he thoughed by output bits and bits The electron-appared is switched by output bits and better corresponding functions. Switching the control of the

Finally, although the Fischer robot is an interesting demonstration model, clearly showing the workings of simple robotics, it is simply too small, too light, and lacking in precision for any useful applications. And we put it offers a worthwhile comparison with an industrial robot, because technically it hardly makes any difference whether one moves tiny parts or heavy loads from one place to another.





85079.5



Zero phase shift between the drive units in an active loudsneaker unit has been the goal of virtually all designers and constructors ever since the first multiple speaker unit was conceived. For a long time. it has been like trying to achieve a perpetuum mobile, but now it has hecome reality!

## phase-corrected cross-over filter

by T Scherer

weakest link in an hi-fi chain: it is the final factor that determines how the hi-fi installation sounds. The most serious problem is that presented by the processing of the wide range of frequencies. As long ago as the thirties, designers have tried to solve this problem by sub-dividing the audible frequency range and using a senarate drive unit for each of the resulting bands. In the simplest case this means that a base speaker (woofer) is used for the low audio frequencies and a so-called tweeter for the high audio frequencies Right up to the 1960s the network that

It is well known that the loudspeaker is the

divides the frequency ranges consisted of a passive filter constructed from chokes and capacitors. When semiconductors became less expensive, designers began to use active filters and to provide each separate drive unit with its own power amplifier that is fitted inside the speaker enclosure. Such active systems are generally better than passive ones, but they are also more expensive. But whether active or passive filters create problems of their own

of a choke in series with the bass speaker and a canacitor in series with the tweeter see Fig. 1. At the cross-over between low and high frequencies, both drive units are fed with the same signal, the level of which is about 3 dB below that of the nominal output at the input to the filter. Moreover, the

signal at the bass speaker lags that of the input signal by 45° while that at the tweeter leads that of the input signal by 45°, Because of the phase difference of 90° between the signals at the two speakers, the air pressures produced by them are added geometrically so that the overall sound is as if caused by a signal that is identical to the original input signal to the filter. Provided that is, that everything is ideal. Tolerances in the components, differences

in the drive units, and effects of the enclosure prevent such an ideal state being attained Even tolerances of 10 per cent can alter the situation quite a lot. If, for instance the capacitance is 10 per cent smaller and the choke 10 per cent larger, the levels at the two drive units are almost 0.5 dB lower than in the ideal case: -3.444 dB. The phase difference is also larger: 95.5°. The result is that the overall signal is almost 0.9 dB lower than the original signal. This may not seem serious, until the considerations concern higher-order filters that give a Ressel Rutterworth, or Chebishev response. Such filters have a much steeper cut-off profile. Even small component tolerances then cause a reduction of a few dR in the available gain The phase characteristic in these filters also has a steeper roll-off. Component tolerances may cause such a large phase shift that the gain is reduced by another few dB. Finally, the loudspeaker characteristics themselves should also be taken into account. The (larger) bass unit is inherently somewhat slower in action than the (smaller) tweeter they have different rise times. The difference between these times manifests itself at the cross-over frequency as an additional phase shift. Odd-order filters have a phase difference of about 90°. In a two-way system with a cross-over frequency of 1 kHz and a difference in rise times of 100 µs (a typical, practical value), there is an additional phase shift of 36° resulting in a total of 126°. Even if all other parameters of the network are one hundred per cent correct, such a phase shift results in a 2 dB loss. In even-order filters, the situation is somewhat better: here an additional phase shift of 36° causes only 0.5 dB loss.

#### Solution

The requirement is, therefore, for a filter that produces no phase shift between the loudspeakers, is not affected by component



Fig. 2. New techniques of filter design enable the construction of this type of filter that in spite of having only two RC sections provides a 12 dB/ octave profile and obviates a number of problems inherent in other types of filter.

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tolerances, is easily reproducible, and, in spite of being a unit for home construction, is absolutely linear.

is absolutely linear.

A critically damped second-order filter (i.e., one consisting of two cascaded simple RC high- or low-pass sections) has the same phase response as an all-pass filter. The filter in Fig. 2 consists of two high-pass sections, A<sub>1</sub> and A<sub>2</sub>. The phase shift, Φ, is calculated from the phase shift, Φ, is calcul

where  $\omega = 2\pi f$  (and f is in Hertz); R is in ohms; and C is in farads.

The phase shift in all-pass filter A<sub>3</sub> in Fig. 2 is determined by

The phase shift vs frequency and gain, G,  $(=U_O/U_J)$  vs frequency characteristics at B, C and D in Fig. 2 are given in Fig. 3. They show that the phase shift at B (output of the

two high-pass sections) is identical to that at C (output of the all-pass section). The only difference between the signals at B and C is that the gain of the former is frequency dependent

$$U_{c(B)}/U_i = \frac{1}{\sqrt{1 + O(1 - DO)^2}}$$
 (3)

$$U_{\alpha(C)}/U_i = 1$$

The output at B only contains high audio frequencies, whereas that at C includes all audio frequencies at the same level. If then the signal at B is deducted from that at C, the gain, G<sub>D</sub>, at D, i.e., Eq. (4) — Eq. (3) becomes

$$G_D = U_{o(D)}/U_i = 1 - \frac{1}{\sqrt{1 + (1/\omega RC)^2}}$$
 (5)

Plotting Eq. (5) shows that the output voltage  $U_{c(D)}$  is very small at high frequencies and is equal to  $U_1$  at low frequencies. The signal at D is therefore the low-frequency output of

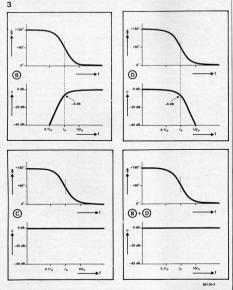


Fig. 3. Box B shows the phase vs frequency and gain vs frequency response at point B in Fig. 2. Boxes C and D show the same for points C and D respectively. Box B+D gives the phase and gain vs frequency response of the acoustically summed B and C outputs of Fig. 2.

characteristic has the form of that of a highpass section: that is the phase of II ... leads that of the input cional This means that there is no phase difference between the high-nace and low-nace branches over the entire frequency range

The dividing filter of Fig. 2 is a two-way version. It has a 12 dB/octave cut-off profile. Normally such second-order filters have two RC networks in both the low- and highpass branches Problems may arise then if owing to component tolerances the time constants in the two branches are not the same These problems are negligible in the set-up of Fig. 2, because due to difference amplifier A., the sum of outputs B and D is always the same as input A irrespective of component tolerances

The cross-over frequency, f., is defined as that frequency where both the low, and the high-pass output are attenuated by 3 dB. This hannons when a RC-1 whence

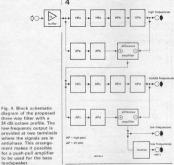
[H<sub>2</sub>]

(6)

### $f = 1/2\pi RC$ Practical filter

The foregoing considerations lead to a practical filter, the block schematic of which is given in Fig. 4 and the circuit diagram in Fig. 5. It concerns a three-way version with a 24 dB/octave cut-off profile: its response resembles that of a critically coupled network ie there is no tendency of overshoot The filter has two low-frequency outputs which are inverted with respect to one another. This offers a simple push-pull amplifier for the bass drive unit, since this often requires more nower than the middle.

and high-frequency speakers. The input signal - Fig. 4 - is applied via a buffer to a four-stage RC high-pass section. and is then available at the high-frequency



the filter Note however that its phase

output Two of the RC stages are designated a: the other two h. This is done to clarify that the phase shift in all-pass section AD is identical to that in the HP, sections: the phase shift in AP, is the same as that in sections HP. - more about this later

The difference amplifier forms from its two input signals a low-frequency output, whose half power frequency f - see Eq. (6) is the dividing frequency between the high- and middle-frequency branches This signal is fed to another four-stage RC high-pass section and is then available as the middlefrequency output The low-frequency outnut is obtained in the same way as the middle-frequency output

#### Circuit description

Onamn A, is the input amplifier whose low-impedance output provides the audio signals for the remainder of the circuit Opamps A2...A5 and A2...A12 are buffers that decouple successive high-pass sections from one another Onamps A. A. A. and Au are connected as all-pass sections with a leading phase shift characteristic. Opamps A<sub>0</sub> and A<sub>10</sub> are arranged as difference amplifiers while A., functions as an investor

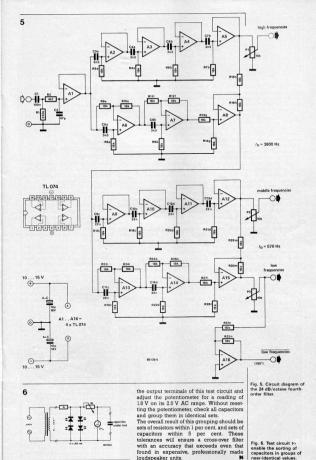
#### Construction

There is no ready-made printed-circuit board available for the filter, but it should fit on a vero board or similar the size of half a Eurocard ie 80 x 100 mm Before the construction is started the two cross-over frequencies should be decided. The relevant component values in Fig. 6 result in crossover frequencies of 570 Hz and 3800 Hz. That is a frequency ratio of 1:6.7 — about two and a half octaves, which is a convenient value. The frequency ratio should not be allowed to be less than 1:4. The cross-over frequencies, f., are calculated from Eq. (6). The next aspect to be looked at is the impedance of the RC networks. To ensure low thermal noise and minimum delays, all resistors should have values between 10 k and 27 k. As the opamps also contribute to noise (see, for instance Intuitive IC Opamps by T M Frederiksen, published by National Semiconductor), the TL 074 should be preferred to the TL 084. Capacitor values are calculated from Eq. (6) once the resistor values have been determined.

Where absolute accuracy is desired, one per cent resistors should be used: these are much cheaper and more easily obtained than close-tolerance capacitors. However, in most cases five per cent resistors are perfectly all right, but it is preferable that resistors with the same letter indices, for instance,  $R_{3a}$  and  $R_{8a}$ , or  $R_{13g}$  and  $R_{14g}$ , have identical values. It is, therefore, more economical to buy, say, fifty 5 per cent 18 k resistors than thirty-two 1 per cent ones, and, with the aid of a digital multimeter, sort out equal-value ones; four groups of threeand eight sets of two identical resistors are required.

Capacitors can be sorted in a similar way see Fig. 6. Connect one of the capacitors to

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loudspeaker units.

elektor india january 1986 1.33

### lithium batteries

hy Frnst Kremnelsauer

For more than ten years, lithium cells and batteries have been used in digital watches, pocket calculators, and pacemakers, but now they appear to be on the verge of coming into much wider use in other electronics equipment. Afready, they are used for direct mounting onto printed-circuit boards, and some [CS have already had them embedded inside their dual in-line packages.

packages. Because of their small size and extremely high energy density, lithium batteries are eminently suitable for use in miniaturized electronic equipment. They operate over a wide range of temperatures (typically –20 °C to +50 °C), have an exceptionally low rate of self-discharer and maintain

their e m f within tight tolerances with

normal loads throughout their life. Unfortunately, at present lithium batteries are not really suitable as direct replacement for conventional dry batteries: apart from their higher price, they do not sand up well to short cirheny do not sand up well to short cirdamaged. Under certain circumstances, they have a tendency to catch fire or even explode. None the less, as already stated, there are economically priced, perfectly safe versions for use in a variety of small electronic for use in a variety of small electronic for use in a variety of small electronic

## Types of lithium battery

Lithium, a silver-white metal that tar-

nishes rapidly in air and reacts with water, halogens, nitrogen, and hydrogen, is, with a density of only 0.531, the lightest alkali metal. It is of particular interest as the anode material in a voltaic cell, where it provides a higher e.m.f. (3.020 V) than other materials.

Because of its reaction with water producing explosive hydrogen lithium poses problems, since it is not

Ithium poses problems, since it is not easy to produce an electrolyte that is completely devoid of water. Sometimes acetonitrile, CH<sub>2</sub>CN, is used. This is a poisonous liquid, prepared from ethyne and ammonia.

The choice of cathode material also has a decisive effect on the cell. This is the reason for the multiplicity of available lithium-based batteries and the differences in e.m.f. and energy density between the various versions Basically, there are two main types of lithium battery: one uses a liquid or gaseous cathode, such as sulphur dioxide. SO., or thionyl chloride. SOCI,: the second has a solid cathode. typically manganese dioxide, MnO., and polycarbonmonofluoride (CE)n In general, solid cathodes are used in small to medium capacity batteries required to deliver relatively low load currents while the other type of cathode finds application in larger capacity batteries that provide relatively high load currents.

It is worth noting that the data sheets of all manufacturers given even more stringent warnings against misuse and abuse of batteries with liquid or gaseous cathodes than those given with lithium cells generally. SAFT, for instance, warns specifically of the danger of explosion and the production of poisonous gases in the use of liquid or gaseous-cathode batteries.

## Construction and properties

Externally, lithium batteries resemble MCd hatteries rather than conventional dry batteries. Both spiral-wound and pressed electrodes are found, again as in NiCd cells. Spiral-wound electrodes have a larger operating surface and are, therefore, able to provide a higher current than pressed electrodes. On the other hand, cells using pressed electrodes generally have a larger capacity-to-volume ratio. Fig. 1 shows the construction of a typical sealed, cylindrical cell with solid (CF)n cathode.

Lithium batteries are available in cylindrical, button, or special shape; the latter, for instance, as a nylon-enclosed memory back-up cell for direct mounting onto a printed-circuit board.

It should be noted that there are appreciable differences in the characteristics of the same type of lithium battery produced by different manufacturers, and also between the various types. For instance, the data sheets of a number of manufacturers give low operating temperatures vary-

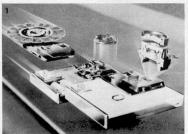




Fig. 1. A typical application of lithium batteries: two BR-2/3A cylindrical cells in a Kodak disc camera provide 6 V (also for flash). They have a nominal capacity of 12 Ah, which, according to Kodak, is sufficient for at least 2000 exposures and an operating life of 5 years is, therefore, guaranteed.

Fig. 2. The smallest cylindrical lithium cell in the world is Matshusita Electric's (Panasonic) BR211. This has a diameter of only 2.2 mm, a length of 11 mm, an e.m.f. of 3 V, a capacity of 5.4 mAh, and weighs only 0.9 q.

2 positive terminal paper insulation lid with coal inculating dies 0000 PVC coating constration fail lithium anodo (CF)n cathoda negative terminal insulating foil 95062 2

+ 123 127 200 energy dens (Wh/kg)

4



ing between -20 °C and 40 °C unner temperatures from ±50 °C to +175 °C (sig): and e.m.f. values of

1.5 V: 2.4 V: 3.0 V: and 3.5 V. To put these values in perspective, brief characteristics of some types are given holow

#### Lithium manganese dioxide.

LiMnO Solid cathode: e.m.f. 3 V: universal type for relatively small currents: can withstand short-circuits for brief periods: operating temperature range typically -20 °C to +50 °C maximum -40 °C to +85 °C canacity about 5 Ah: intended for use as backup battery for CMOS RAMe

#### Lithium carbon monofluoride. LICE

Solid cathode: e.m.f. 3 V: universal type for small to medium value currents up to 150 mA: higher current pulses up to 1.4 permissible cannot withstand short-circuits; operating -40 °C to temperature range +85 °C: capacity up to 5 Ah; intended for use in camera flash equipment. distress-signal transmitters distress lights, and memory back-up.

#### Lithium conner ovide LiCuO

Solid cathode: e.m.f. 1.5 V: universal type for load currents up to 1 A: operating temperature range -20 °C to +55 °C (some manufacturers claim up to +135 °C); capacity up to 20 Ah; intended for use in distress-signal transmitters and equipment operating at high temperatures.

#### Lithium copper oxyphosphate. LiCu,O(PO,)

Solid cathode: e.m.f. 2.4 V: derived from LiCuO cells; and properties therefore generally similar to those, but temperature range extended to -40 °C to +70 °C (for special appliFig. 3 Sectional view of a lithium carbon monofluoride cell

Fig 4 Energy density of a number of

Fig. 5. The capacity vs storage period curve shows that lithium batteries even after 10 years storage have more than 90% of their canacity left

Fig. 6. Output voltage vs discharge neriod curves for various temperatures

Fig. 7a. Output voltage vs discharge current curves at various temperatures of a lithium thional chloride cell

Fig. 7b. Output voltage vs discharge ablacida asti









	ZnMnO <sub>2</sub>	LiCuO	LiCu,O(PO,),	Li(CF)n	LISOCI,
Nominal e.m.f.	1,5 V	1.5 V	2.4 V	3.0 V	3.5 V
Recommended load current Maximum constant	80 mA	40 mA	40 mA	150 mA	ca. 100 ma
current	≥1A	0.6 A <sup>11</sup>	ca. 0.3 A <sup>2)</sup>	0.3 430	0.6 441
Current pulses	≥ 1 A	≥ 1 A	≥ 1 A	≥ 1 A	> 1 A
Nominal capacity	5.5 Ah	10 Ah	5 Ah	5 Ab	5 Ab
Operating temperature	20 °C	-40 °C	0 °C	-40 °C	-40 °C
	+70 °C	+60 °C	+ 175 °C	+80 °C	+60 °C
Energy available	8 Wh	15 Wh	12 Wh	15 Wh	18 Wh
Weight	65 g .	56 g	55 g	47 g	56 g

at 20 °C for 75% nominal capacity. 2) at 150 °C for 75% nominal capacity

<sup>4)</sup> at 20 °C for 50% of nominal capacity.





Eig 9 Characteristic curves of a lithium carbon monofluorido cell: (a) output carbon monofluoride cell: (a) output (h) output voltage vs. discharge time: (c) output voltage ve discharge time for difformat look of the

Fig. 9. Output voltage vs discharge time curves of an alkaline manganese a carbon zinc and a lithium thiomal chloride 9 V cell Load 636 ohms: tem perature 25 °C

Fig. 10. Output voltage vs discharge time curves of a 9 V lithium thionyl chloride cell at different operation temperatures

Fig. 11 Various types of lithium





cations up to +175 °C); capacity up to E Ah Lithium thionyl chloride, LiSOCI.

available in higher capacities and for higher load currents

Many lithium hatteries can cone with the momentary short circuit during dip soldering but normally require hours to recover their e m f. If a battery falls into the soldering bath it may explode: it is therefore essential that it is securely fastened to the board or equipment being soldered.

#### 8c

a



tune for load currents up to 2 A. operating temperature range -40 °C to +75 °C; capacity up to 18 Ah; intended for use in measuring instruments and communications equipunder difficult ment operating conditions

Liquid cathode: e.m.f. 3.5 V: universal

hattery are suitable for general use yet. This has not so much to do with the price as with the care that needs to be taken by the user. Although short circuits do not necessarily cause an explosion, account must be taken of the tremendous rise in temperature (to well over 100 °C). Under these conditions, pressure inside the cell will rise. causing the safety valve to open and health damaging gases to escape. Batteries (consisting of more than one cell) are normally protected against short circuits by a fuse or series resistance.

Lithium cells used as back-up for memory ICs should be provided with a protection diode to prevent any tendency to charge and also to avoid large discharge currents.

batteries are, however, provided with soldering tags at their terminals, but even these should not be subjected to soldering heat for more than 10

The very low rate of self-discharge allows small charge and discharge currents - of the order of a few µA - so that these batteries may be charged from solar cells.

whether these batteries will become

Application and use

It appears that not all types of lithium

Soldering direct to the battery terminals is not permissible. Many lithium seconds

At the time of writing, it is not known

#### State of the art

SAFT have produced a replacement for conventional 9-volt PP3 hatteries that demonstrates the advantages of lithium batteries in an impressive manner. This new battery consists of two button cells of the LiSOCI, type, and thus provides an e.m.f. of 7 V. Since this voltage remains stable during the operational life, the battery is perfectly suitable as a substitute for a PP3. In contrast to most other lithium batteries, this new type stands up very well to short circuits: its temperature rise during such conditions rises only moderately. From a technical point of view, this battery would have to be recommended for any application requiring a 9-volt source. Unfortunately, at a retail price of well over £10, it is not going to replace too many PP3s just vet.

Characteristics

Output voltage Recommended load

current (for 50%

Weight

Operating

of nominal capacity at 20 °C)

temperature range

Nominal capacity





7.0 V 50 mA 30 g 40 °C to +70 °C

1.1 Ah

Rechargeable lithium batteries Although much research has been car-

ried out, a number of patents have





Fig. 12. This is the size of Panasonic's rechargeable lithium carbon battery.

Fig. 13. Charge-discharge curves of Panasonic's rechargeable lithium carbon hattery

Fig. 14. Discharge characteristics of Panasonic's rechargeable lithium carbon battery.



been registered, and several prototypes have been publicized, there is, at present, only one rechangeable lithium battery in production. This is a Panasonic lithium carbon type, first introduced in early 1984. Production models are expected to become available in Europe during 1986. The most important characteristics are

available in Europe during 1986.
The most important characteristics are shown in the table below and in the accompanying charge and discharge curves. These curves show the truly amazing property of this battery, which enables the output voltage to be freely chosen between 1.5 and 3.0 V dependent upon the charging voltage. As regards the life-span, the makers claim that even after 2000 characterism that even after 2000 characterism.

discharge cycles the capacity shows

#### Characteristics

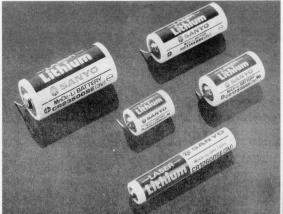
Thickness

Weight

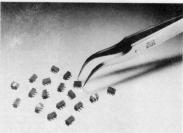
Nominal 1 mAh
capacity (at 2...3 V)
Nominal voltage 3 V
Recommended 1 μA to 5 mA
load current
Charging 1.5 to 3.0 V (convoltage stant) with current

limiting resistance
Life expectancy
At least 2000
charge-discharge
cycles
Diameter
10 mm

10 mm 2 mm 1.9 g



## surface-mount technology



Surface-mount technology uses components that are much smaller than conventional ones. These SMDs surface-mount devices have no or vary short connecting terminals since they are intended to be soldered direct to the capper tacks of a circuit board. The control of the capper tacks of a circuit board, so provided the capper tacks of a circuit board, or an expected that within the years tail of all electronic circuits will use SMDs, while within another five years it will be hard to find current type com-

Most inanufacturers engaged in the electronics industry are already firmly committed to surface-mount technology. This has come about not only because of further miniaturization, but also — and perhaps more importantly — because these manufacturers are under pressure to reduce their assembly costs.

#### Advantages of SMA

#### (surface-mount assembly)

Currently, the cost of an electronic circuit is determined primarily in the assembly and not by the components. This has come about because component manufacturers have continuously invested heavily in the development of better, smaller, and cheaper components, whereas equipment manufacturers have hardly changed their production methods since PCB assembly was automated. Relatively pseuking, theelon, assembly costs appealing, therefore, assembly costs of the cost of the

of the printed-circuit board, the holes, and the cost of plating, together with the increase of the board density and complexity, is the key factor of surface-mount technology. This in spite of the fact that SMDs are currently still more expensive than conventional components.

SMDs have connecting terminals of repartition of more repartition. If more release, whereas conventional components require at least 2.5 mm. This means not only a 70 per cent saving on board space (National Semi-conductor fledge, but also one third of the internal semiconductor leads.

substantially reduced parasitic parameters, which is of particular importance in high-frequency circuits. Reliability is of prime concern to an engineer. Most component manufacturers have run extensive reliability programmes, which show that the reliability of SMAs is better than that of conventional PCBs.

#### Some hidden difficulties

It must be pointed out that there are also unforeseen difficulties with surface-mount assembly, but only in existing system designs. These designs are often poorly partitioned for surface-mount realization, and this is hampering the introduction of surface-mount techniques by original equipment manufacturers (DEMs).

The difficulties originate in the current practice of including a mixture of control and associated higher dissination interface circuits on PCBs As these higher power components are not yet available in surface-mount the designer is left with the choice of either using the unpopular mixed print board with a combination of throughhole and surface-mount devices, or repartitioning the design into surfacemount and through-hole hoards. Such repartitioning represents a major design investment, which is difficult to justify for an existing system and, therefore, tends to restrict the use of surface-mount techniques to new

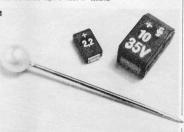


Fig. 1. Surface-mount tantalum capacitors from Siemens. Compared with the already small conventional tantalum capacitor, the SMD type is 80 per cent smaller.

#### Surface-mount devices

The photographs accompanying this article show that SMDs, companied with conventional components, have a rather different appearance: ministure blocks and cylinders, timy (2s with venily short pins, and other unfamilie exteriors. As far as their interiors are no basic differences other than that SMDs are generally of better quality than their conventional counterparts. An important aspect is that SMDs are designed to withstand immersion into motters odder.

Rather than ask which components are already available in surface-mount technology, ask which ones are not yet technology, ask which ones are not yet vanished. Every surface that the control of conventional components have a surface-mount counterpart, be they resistors; ceramic, electrolytic, or tani-tablum capacitors; diodes; transistors; CICs; even inductors and LEDs are already available in surface-mount.

#### Passive components

Surface-mount resistors are available in values from 1 ohm to 10 magohms, with tolerances of ±5%; ±10%; and ±20%. The construction of such a resistor is shown in Fig. 2a. It consists of a rectangular ceramic carrier onto by which a layer of resistive material is deposited that is cut to its exact value by a laser. The whole is glazed for propagations of the construction of the constr

Ceramic capacitors are available in surface-mount in values from 0.47 pF to 1 uF. The value affects the dimensions, of course, and there are, therefore, quite a number of formats. All types have the same working voltage: 50 V (IEC standard). A typical construction is shown in Fig. 2b. Screened electrodes are pressed onto ceramic wafers, after which the wafers are pressed together, protected by foil. and then cut into small blocks. Finally, the two terminations are attached. Electrolytic capacitors - see Fig. 2c come in values from 0.1 µF to 22 µF and working voltages from 6.3 V to

Electrolytic capacitors — see Fig. 2c — come in values from 0.1 µF to 22 µF and working voltages from 6.3 V to 63 V. They are constructed from etched aluminium foils, which are separated by paper impregnated with electrolyte. The tubular aluminium seems of the compact of the

Tantalum capacitors come either as chips or in moulded form; values of the former range from 0.1  $\mu\text{F}$  to 100  $\mu\text{F}$ , with working voltages of 4 V to 50 V; the latter are available in values from 0.068  $\mu\text{F}$  to 100  $\mu\text{F}$ , and working voltages of 3 V to 50 V. A typical moulded tratularum capacitor is shown in Fig. 2d. Chip types have body coats of tough epoxy resin.

#### Active components

Virtually all current transistors and diodes can be produced in surface-

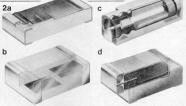


Fig. 2. Construction of respectively a resistor (a); a ceramic capacitor (b); an aluminium electrolytic capacitor (c); a moulded tantalum capacitor (d); all in surfacemount. These components are all designed to withstand a brief immersion into



Fig. 3. A number of different case styles of surface-mount semiconductors: SOT-23 (a): SOT-89 (b): SOT-143 (c): and SOD-80 (d).

mount technology without any difficulty: too many to describe in detail. There are general purpose, switching, high-frequency, low-noise, field-effect; and high-voltage transistors. Diodes are available from zener. Schottky, and

cooces

Fig. 4. Already there is ample choice of surface-mount ICs. Shown here is a typical example in a SO-14 package.

switching to variable capacitance types. Various types of case are shown in Fig. 3: (a) SOT-23; (b) SOT-89; (c) SOT-143; and (d) SOD-80.

#### Integrated circuits

Many familiar ICs are already available in surface-mount technology; digital as well as analogue; TTL as well as CMOS. Inverters; multivibrators; buf-fers; decoders; multiplexers; shift registers; voltage comparators and regulators; timers; phase-locked loops; video amplifiers; digital-to-analogue converters; sterso decoders; IF amplifiers; and many more.

As far as cases are concerned, there are two basic styles, the length of which varies with the number of pins. Fig. 4 shows an SMD-IC in a 14-pin

SO 14 care

The quality of SMD-ICs is like that of transietors and dindes at least as mond as that of DII types since the same tune of coestale are used. The smaller nackage results in a lower permissible discination however

Note that Hall-effect SMD-ICs are also available

#### Mounting SMDs

For mass production purposes SMDs are nacked in blister tape — see Fig. 5. The tane protects the components and ensures that they are efficiently processed by the automatic mounting equipment. Mounting of SMDs in quantity production is of course fully automatic: that was the whole idea hehind the new technique. It is hevond the scope of this article to give other than a brief description of this automatic process

The most commonly encountered procase uses droplets of thermal hardening epoxy glue which holds the SMDs temporarily in position. The glue may he applied to the substrate (the circuit board) or to the components. After the glue has been hardened, the components are attached to the board by two stages of wave soldering; one to ensure that all metal surfaces are prowided with sufficient solder and the second to remove any excess of solder. Home constructors, of course, have no access to automatic mounting equipment and wave soldering haths and they will, therefore, have to mount SMDs with small pincers and a mini soldering iron. None the less it is advisable to glue the components in place prior to soldering. Glue should be applied with the sharp end of a pin. Soldering should be done very carefully and the tip temperature should he electronically controlled. A special surface-mounting solder cream is produced by the Indium Corporation of America and is available in the UK from Dage (GB) Limited.

SMDs may also be attached to the board with special conductive enoxy. which is, however, quite expensive. Where this epoxy is used, it should be hardened at 150 °C for not more than 60 minutes. A termination material that will not oxidize such as gold plating, should preferably be used if the epoxy forms the electrical connection between the component and the

#### The future

It is clear that surface-mount technology is not a whim that is forgotten tomorrow; it is the assembly technique of the future. It is also still in its infancy, and dynamic development will no doubt continue for some years. At present, the technology is really only suitable for automatic mounting equipment in factories, but, no doubt, equipment for the smaller producer

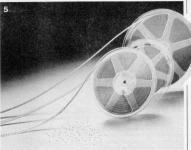


Fig. 5. For mass production, SMDs are delivered in blister tape, i.e., a series of compartments separated by a thin polythene tape. The tape can be fed into auto matic mounting equipment. SMDs are, however, also available in different packing

will become available foreseeable future. It is to be hoped, available from: that in the further development, the one-off producers, the hobbvists, will • Rabans Lane • Aylesbury • not be forgotten and then for them, too, there will become available suitable tools for use with SMDs.

in the Surface-mounting solder cream Dage (GB) Ltd • Intersem Division

Bucks HP19 3RG . Telephone (0296) 33200

Sources. Transistors & diodes for surface mounting (ITT) SMD Technik (Siemens) Oppervlaktemontage (Philips) Surface-mount Components

(Sprague)

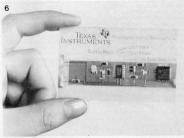


Fig. 6. A surface-mounted board has a different appearance than a conventional PCB. This sample board houses a simple flashing-light circuit.

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## check list for electronic fault finding

## or 'where and how to look for what that doesn't'

#### Before soldering in components

Check that the components agree with the parts list (value and power of resistors, value and voltage rating of capacitors, etc...). If in any doubt, double-check the polarized components (diodes, capacitors, rectifiers, etc...).

If there is a significant time lapse between last reading an article and building the circuit, take the trouble to re-read the article; the information is often given in very condensed form. Try to get the most important points out of the description of the operation of the circuit, even if you do not understand exactly what is supposed to harmonia.

If there is any doubt that some components may not be exact equivalents, check that they are compatible.

Check that they are compatible.
 Only use good quality IC sockets:

 Check the continuity of the tracks on the printed circuit board (and throughplated holes with double-sided boards) with

a resistance meter or continuity tester.

Make sure that all drilling, filing and other 'heavy' work is done before mount-

ing any components.

If possible keep any heat sinks well

isolated from other components.

Make a wiring diagram if the layout

 Make a wiring diagram if the layout involves lots of wires spread out in all directions.

Check that the connectors used are compatible and that they are mounted the right way round.

Do not reuse wire unless it is of good quality. Cut off the ends and strip it anew.

#### After mounting the components

- Inspect all solder joints by eye or using a magnifying glass and check them with a continuity tester. Make sure there are no dry joints and no tracks short circuited by poor soldering.
- Ensure that the positions of all the components agree with the mounting diagram.
- Check that any links needed are present and that they are in the right position to give the desired configuration.
- Check all ICs in their sockets (see that there are no pins bent under any ICs, no neighbouring ICs are interchanged, etc...).

 Check that all polarized components (diodes, capacitors, etc...) are fitted correctly.

• Check the wiring (watch for off-cuts of component leads), at the same time ensure that there are no short circuits between potentiometers, switches, etc. ..., and their immediate surroundings (other components or the case). Do the same with mounting hardware such as spacers, futts and holts. etc.

 Ensure that the supply transformer is located as closely as possible to the circuits (this could have a significant influence in the case of critical signal levels).

Check that the connections to earth are there and that they are of good quality.

 Check that any pins, plugs or other connectors used are making good contact.

 Make sure the circuit is working correctly before spending any time putting it into a case.

#### And if it breaks down . .

- Recheck everything suggested so far.
   Reread the article carefully and clarify
- Reread the article carefully and clarify anything about which you are doubtful
- Check the supply voltage or voltages carefully and make sure that they reach the appropriate components especially the pins of the ICs (test at the pins of ICs and not the soldered joints!).
- Check the currents (generally they are stated on the circuit diagram or in the text). Don't be too quick to suspect the ICs of overheating.
- If possible check the operation of the circuit in separate stages. As a general rule, follow the course of the signal.
- Check the contents of any PROMs or EPROMs fitted.
- While checking voltages, currents, frequencies or testing the circuit with an oscilloscope, work systematically and take notes.
- It is always a good idea to do any fault finding as a combined operation with a friend, two heads are better . . .
- Be wary of 'red herrings' when fault tracing. Do the simple checks first.
- Finally, remember our constant companion Murphy is looking over your shoulder. If that part of the circuit cannot possibly be wrong and you haven't checked
- it that's where to start looking.
   ... And don't forget to switch the power on and check the fuses!

## Channel multiplier for flat TV panel

Scientists of the Philips Research Laboratories in Redhill, Surrey, have achieved a flat cathode-ray tube with a picture diagonal of 12 inches and normal TV resolution. The depth of this tube is less then 3 inches. The first flat, sealed-off monochrome tubes have been made. The problems of gain stability have been overcome and an acceptable operating life can now be obtained. The feasibility of achieving full colour has been demonstrated. For the near future applications are expected to be in professional use only.

#### System

The flat cathode-ray tube (see Fig.1) consists of an electron gun, deflection plates, an electron multiplier array, a phosphor screen, and a faceplate that is vacuum sealed in a metal can. Because of the electron multiplier, the electron beam can be of both low current (less than 1 µA) and low energy (400 eV). The electron beam travels down the back of the tube to a reversing lens where it is turned through 180° into the front section. A central partition carries a series of frame deflection plates which create a field to turn the beam forward on to the multiplier. The current from the gun is amplified several hundred times by the before the beam is accelerated to the screen. Because of the low primary beam energy and current, the scanning system can be unorthodox. Vertical scan is achieved by progressively ramping the potentials on the frame plates. Electrostatic deflectors near the gun provide the line

#### State of the art

Much progress has been made concerning the picture area and resolution. The spacing between the multiplier channel centres has been reduced to 0.55 mm, providing appr. 170 000 channels in the 305 mm diagonal display. The resolution of the screen image and the grey scale capability is appropriate for TV applications. The spot size is such that the resolution of the tube is limited by the pitch of the channels in the electron multiplier. The main factor which determines the life of the flat display tube is deterioration of multiplier gain. Multiplier tests show that after 7500 hours of continuous operating the gain

falls to 63% of its original value. Colour is important for many professional applications and several methods have been studied. The presence of the electron multiplier poses problems which are very different from those of a shadowmask tube. Colour selection can be carried out either before or after the multiplier. If the selection process takes place before the multiplier then one channel must be dedicated to each primary of a colour triad. This limits the maximum colour-display resolution to one third of its monochrome resolution. The Philips Research Laboratories are studying methods in which a system of electrodes on the output of the multiplier directs the emerging electrons onto phosphor of the desired colour. The ultimate tube design has one gun, and sequential colour selection is therefore needed. The low deflection voltages and the high nicture brightness make the tube particularly suitable for this mode of operation. Two methods are being studied, the dots-and-rings method and the deflection method.

#### **Dots-and-rings** method

The electron source inside each multiplier channel is a ring which is imaged on to the screen. A system of dynode-like electrodes at the multiplier output can be made to act as a lens of variable focal length, enabling the size of the image to be altered. The phosphor triads on the screen consist of concentric patterns in the three primary colours, which are aligned with the multiplier channels. The emergent electrons from each channel can be focused into a spot exciting the red phosphor, a ring exciting blue, or a larger ring exciting green.

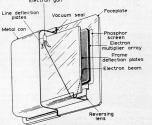
#### Deflection method

With the deflection method, the screen consists of a pattern of phosphor strips in the three colours. A positive voltage applied to a dynodelike extractor electrode is used to draw the electrons from the final multiplier stage. They are then deflected on to the desired colour by pairs of strip electrodes located between adjacent rows of channels. The strip electrodes and the extractor electrode form an asymmetric lens which causes the electrons to be focused on the screen as an elongated spot.

The results of both methods obtained so far are close to the requirements for various professional applications (such as data display) where flat screens are important; the possibilities for domestic applications are being studied. The practical work is carried out in demountable vacuum systems with small-area multipliers (2 by 2 inches). They now need to be developed into a large-area technology.

The results described here refer purely to laboratory research; they in no way imply the manufacturing or marketing of new products.





# Microprocessor navigation

by Kevin Desmond

The miniaturization and computerization of electronic navigational aids can only benefit today's motor yachtsman as he voyages through increasingly crowded waters and marinas.

His means of knowing his speed, direction, and the depth have been made simpler, largely thanks to the sophistication of microprocessor controlled alphanument displays, which are easy to recall and read and are small enough to fit into a handbag. Take, for example, the Triton F.15 as developed by Baron Instruments (\*).

lake, for example, the Intron I-to as developed by Baron Instruments (1). Here is a 15 function yacht instrumentation system, fitted into a console measuring a mere 234×171×50 mm. It is designed with the express purpose of easing the congestion of a whole array of instruments.

The functions are boat speed; velocity made good; total log; resettable log; depth in feet; depth in metres; depth in fathoms; true wind speed; apparent wind speed; true wind speed; apparent wind speed; true wind speed; apparent wind speed; true wind speed; speed with speed spee

#### Digital display

These functions may be recalled on a large digital display which makes use of a six digit, 25 mm, liquid crystal system. This is backlift for night tillumination and also fitted with a nami glate window. Another 14 light emitting diode indicators in traffic light red are positioned immediately beneath the tool of the control of the co

Using the latest generation 8031 microprocessor, Triton F.15 works off a 12 or



The Triton F.15 is a 15 function yacht

instrumentation system.

24 V supply, consuming only 300 mA at 12 V. When the power is turned off, the clock calibration and alarm settings are retained by an internal nickel cadmium battery, rechargeable off the ship's supply to give up to six months' backure.

Existing Baron water speed and depth transducers can be used with the Triton F.15 system.

As with the voice advice system used on the Austin Meastro and other cars, so has Seafarer (<sup>2</sup>) made it possible for the yachtsman to be "told" his depth, enabling him to focus his eyes on other tasks in hand. The Echovox talking repeat meter can be used in conjuction with the 110 m Seafarer 5 and the 183 m Seafarer 700 echo sounders.

#### Clear voice

Metric and imperial versions are variable, offering presentation of soundings both through a numerical digital display and a variable volume, synthesized, clear English voice. Soundings are given in units and decimal parts of a unit in depths of less than 10 m. The voice repetition rate increases in shallowing water and a shallow water alarm signal can be present to any depth up to 10 m.

Echovox operates from a ship's supply of 12 V dc and measures a mere 158×168×75 mm.

If you do not like voice synthesis, there is the Navsounder, as developed by Stowe Marine Equipment <sup>51</sup>. This is a microprocessor controlled digital depth sounder, with alarm settings that can be selected individually at either station — so that the yearhsman does not venture into water that is too deep or risk running aground.

The liquid crystal display gives a digital presentation to a range of 100 m. In this particular unit, the eight-bit micro-processor, with 2 K of fixed random access memory, gives special capacity to selectively process and interpret acoustic signals. Secondary acoustic signals. Secondary acoustic signals caused by turbulance, debris and fish are rejected and do not appear on the display. Deep and shallow alarms can be set to

the nearest foot, fathom or metre, and allow for keel offset. Audible deep and shallow alarms are distinguished from each other by tone, with dashes for deep, and dots for shallow. When selected, the 'anchor watch' function continually monitors depth and warns of abnormal changes.

#### Universal sensor

Navsounder operates from a ship's supply of 10 to 12 V and 80 mA, with 60 mA lighting when required. It measures 110×110 mm.

Moving from depth to direction, development of the age old compass has certainly not stood still. This is evident in the Meteor Digitrac electronic compass system produced by

Marinex<sup>(4)</sup>. The detection of the earth's magnetic field with a solid state, electronically

field with a solid state, electronically damped universal sensor, enables up to five remote compass displays, both analog and digital, to be actuated without any extra circuitry.

Among these displays are an analogue pointer display; an analogue head-up display unit with rotating card or grip pointer options; a digital display unit; and a microprocessor controlled tape repeater with liquid crystal display. The Meteor system can also be used for satellite navigation, automatic direction finding, and autopilot interface. The all-important universal sensor unit offers automatic compensation for changes in the horizontal field strength, and maintains absolute voltage control outputs. It is also constant over angles of dip of up to 80 degrees. This aids helmsmen of both power and sail boats to maintain an accurate course, even in very rough conditions. By allowing analogue and digital displays to be inter-wired, both helmsman and navigator can see what is happening, on different types of compass.

#### Ship to shore

Apart from human-controlled direction, there is automatic control. With the Wheelpilot 4000, developed by Navico<sup>55</sup>, the functions of rudder ratio, sea state, and trim are entered via keyboard control. Each and every command is confirmed on the liquid crystal display and by a bleep.

There is also an off-course alarm, a port and starboard dodge facility, and a highly efficient gearbox. During the design stage, Navico researchers paid a great deal of attention to waterproofing, so that special seals and double gaskets were incorporated to function even in the wettest and roughest of sea conditions.

Last but not least, the ability to communicate ship to ship and ship to shore must be regarded as a vital aid to navigation. Navico has also come up with the first totally British designed and built mircocomputer controlled VHF radio telephone for yachtsmen.

radio telephone for yachtsmen. With its black casing 190 mm wide by 89 mm high, the Navico RT 6100 also of handbag size. There is a scan of up to ten channels, each easily programmed via a large keypad, with entry confirmed by both an alphanmeric liquid crystal display and a bleep. There are also six private channels, a first microphone or telephone handset option, and a selective calling system option.

These and other electronic navigational aids under development by

small and enterprising British companies inevitably lead one to either dream of, or dread, the time, in the not too distant future, when computerized, voice synthesis, navigator robots will do all the work, leaving the motor yachtsman the uncluttered leisure of such pastimes as fishing, photography, and even sunbathing. (LPS)

Baron Instruments Ltd,
 West Wycombe Road, High
Wycombe Buckinghamshire,

England, HP11 2LG.
2. Seafarer Navigational International
Ltd, Fleets Lane, Poole, Dorset,
England, BH1 5BW.

3. Stowe Marine Equipment Ltd, 1 Bowes Hill, Rowlands Castle,

1 Bowes Hill, Howlands Castle, Hampshire, England, PO9 6BP. 4. Marinex, 11 Balena Close, Creekmoor, Poole, Dorset, England, BH17 7DB.

 Navico, 49 Harbour Parade, Ramsgate, Kent, England, CT11 8LJ.

# Protecting computers from fraud

by Cheryl F Williams

In the enthusiastic rush to gain the full benefits of computers, few companies seemed to have thought of the disadvantages and, in particular, the problems of computer security.

Computer crime is a new growth industry of the 1990s, with rich pickings being available. Many frauds are never made public and the increasing use of computers in routine commercial work offers increased scope for this type of crime. Even the most sophisticated computer lacks the human attribute of commonsense and so any transaction that conforms to the computer's rules will be processed.

Computer frauds require a knowlegde of the system's characteristics and could involve tampering with data, programs or software. The perpetrators vary widely from trainees to senior management, and the sums involved range from tens to millions of pounds.

#### Heavy losses

A study of computer crime by an American Bar Association committee says that organizations often did not know who had committed a crime; many did not know when a computer crime had taken place and could not monitor their systems to detect it.

In a survey of 283 large corporations and government agencies about 48% reported some form of computer crime in the last year with losses conservatively estimated at between \$145 million and \$730 million.

A recent "Washington Post" series on computer crime suggested that annual losses may be between \$100 million and \$3000 million. No one knows with any certainty how many millions are

going missing, and the problem is snowballing.

Open Computer Security<sup>(1)</sup> comments: "For every one computer crime that is reported it is estimated that probably a further 20 go completely undiscovered. Such frightening figures indicate two things.

#### Coded messages

"First, without the proper safeguards almost every computer is prone to this type of attack and, second, there are more individuals than you would imagine who have the required degree of technical knowledge to carry out armchair robberles. Indeed, computer the control of the decade, all cases of fraud would involve a computer."

Open Computer Security goes to great lengths to ensure that its systems are secure. It says: "Due to the extremely high levels of encryption and verification which are built into our systems, not even we are able to overcome the security procedures inherent to our finished and installed properlia unthentication codes from there are special authentication codes built into our security continues are successful authentication.

"This means that the host computer will accept instructions only from another computer that has previously been given clearance. Also, if the temper-proof box is opened the memory is instantly wiped clear and the system goes into alarm. Every message that goes through the system automatically has an authentication

code attached in it which tallies with the contents of the message.

"This means that an instruction to transfer \$1000 cannot be changed to \$1000 000 — either by operator intervention or a fault on the line — without the change being high-lighted."

#### Automated security

Open Computer Security has designed and manufactured the authentication equipment for CHAPS, the Clearing Houses Automated Payment Scheme in London. CHAPS will replace the physical carrying of large cheques about the City of London by messengers, Approximately \$37.000 million a day is handled between different clearing banks, the clearers and the Bank of England.

CHAPS uses the data encryption standard (DES) as its basic scarabiling device and the session key, which is changed daily, is held in a tamper-resistant module designed by Open Computer. The module fits into the authentication unit attached to the tandem gateways in each of the clearing banks.

The key itself is a series of random numbers produced by electronic noise and no one, neither the users nor Open Computer staff need know what it is. CHAPS has a way of identifying each bank's module to prevent substitution. The authentication unit is designed to detect and reject any message that has been interfered with.

Many specialists believe that it is one of the most secure systems in the world and it is becoming a model for other financial institutions. One expert has commented: "CHAPS is more

secure than an armoured car - you are not going to get in there".

#### Award winner

Open Computer Security has won the British Computer Society/Computing Applications Award for its Padlock system which was designed to prevent software piracy. Padline 7, a more recent development, is operative at all seven levels of communication as defined by the International Standards Organisation in its OSI model. This allows the unit to be used in virtually all computer networks whether public such as X25 - or private - such as SNA

From the most basic physical layer level 1 - Padline will perform through X25 layers right up to level 7. Here, encryption takes place under the control of the user's actual applications software making this, the manufacturer believes, the most secure method vet devised. This "end to end" level gives total network transparency under all communications protocols. Data encryption is via the accepted DES

algorithm. Special circuitry in Padline allows the generation of truly random keys whilst the RSA public key provides a secure method of transporting the keys over non-secure networks, so that the user can confidently design a complete key management system. The codes used in Padline 7 are housed within one of the machine's microprocessors.

To prevent the possibility of these codes being misused, the entire cabinet is designed as a totally sealed unit. Padline is without vulnerable air vents and, if it is tampered with, the memory is instantly wiped clear and alarms are triggered. Data can be loaded into the unit via its cassette input socket so the user can upgrade Padline on site - perhaps to operate on a different communications level from a supplied tape.

#### Double locked

For security reasons, field upgrades can only be carried out in the presence of both keyholders. Additionally, the tape (which is prepared to order) is programmed to match only the particular Padline for which it was intended

Racal-Milgo(2) has designed the Datacryptor II which operates by rearranging the digital bit pattern of information into an indecipherable stream. This is achieved with the DES algorithm. By using this in a single bit cipher text feedback mode, Datacryptor II achieves a high degree of security with true protocol independency. Both asynchronous and synchronous protocols can be managed by Datacryptor II operating in full or half duplex modes over point-to-point and multidrop networks. The unit will also operate over leased line or dial-up circuits. A point-to-point link merely The Racal-Milgo Datacryptor II.

involves two Datacryptor II units one at both central and remote sites. Each unit has a small, hand held. removable memory device known as a key transport module (KTM).

The KTMs are normally double locked inside the Datacryptor II units and without them the system will not operate.

#### Master key

The Datacryptor II has randomly generated 64 bit keys. The user does not see these keys or have any influence over their generation. The initial or master key is generated at the central site Datacryptor and is loaded into two KTMs. One KTM is then transported and loaded into the remote site Datacyptor and the other is left at the central site unit

The central Datacryptor generates a working key which is down-line loaded to the remote site. This operation is quickly and simply carried out at the central site Datacryptor II front panel. Working keys may then be changed as often as required

at both ends of the link - simply by pushing a button at the central site. The master key is also easily and quickly changed simply by generating a new key for loading into central and remote sites.

Down-line key loading provides an additional degree of security against the presistent line monitor attempting to determine the working key. This dual level of keys therefore ensures a higher level of security for sensitive data networks and avoids costly and time consuming procedures of constant transportation of keys.

All key management and control functions are performed at the central site Datacryptor where key generation and

All controls and the KTM are double locked behind a front panel which can only be accessed by the operation of two security key locks.

#### Information rejected

Once accessed, the controls consist of three pushbuttons, one to initiate the master key, one to copy this information into the second KTM, and one to generate down-line loaded working keys. The front panel LED indicators display transmission and security

status Test indicators allow checking of keys and the data link. Datacryptor II is housed in a robust and secure package. Should any unauthorized access to the unit be attempted, the anti-tamper switches will automatically erase the keys, disabling the system, and maintaining the integrity of the data network.

In the event of a power failure, the working key and module security code are protected by a nickel-cadmium battery for a minimum of 1000 hours. As a standard feature, Datacryptor can be securely mounted on a desk top or in a rack. Removal requires the use of two keys. Datacryptor can be enhanced by the expansion module option which provides a module security code. This allows the user to provide each remote site with an individual address so that, unless the correct KTM is received, the key information is rejected.

- 1. Open Computer Security Ltd, 31-32 High Street, Dorset Place, Brighton, East Sussex, England, BN2 1RP.
- 2. Racal-Milgo Ltd, Richmond Court, 309 Fleet Road, Fleet, Hampshire, England, GU13 8BU.



After the preliminary and theoretical considerations of the past three months, this fourth article in the series deals with the construction of the main printed-circuit board. The board is Euroformat (100×160 mm), double-plated, and has through-plated holes: clearly not a card for home production.

It is not absolutely necessary to have read through the three previous articles, but it does help. It should be noted, however, that the circuit diagram was published in Part 3



high-resolution colour graphics card - 4

(December issue), and this, of course, is essential knowledge. The construction of an essential knowledge. The construction of a construction of the construct

The ICs may be mounted in good-quality sockets but at least as far as the dynamic RAMs are concerned, it is better not to If any faults manifest themselves, do not immediately suspect the ICs: experience shows that in the initial stages most faults are not caused by faulty components, but rather by suspect workmankin, If, in spite of all this, it is found that an IC is at fault, just cut off the plans. The properties of the plans from the holes in the board. If you have a desoldering device available so much the better.

Although the board is a very reliable component, it often pays to inspect it carefully (and possibly with a magnifying glas) for hairline breaks in the tracks. This can save a lot of tedious work later.

#### Fitting the components

First, fit the five wire links. Since these carry a reasonable current, they should be made of relatively heavy insulated copper wire. The GDP (graphics display processor) is best fitted in a really first-class socket. DIL switches  $S_1 \dots S_6$  are soldered direct to the board.

Make sure that the quartz crystal used is housed in a HCl8U or HC2SU case, and that its frequency is suitable for the GDP used (14 MHz for the 9365 or 9366, and 12 MHz for the 9367). Where available,  $R_1...R_8$ ,  $R_9...R_{15}$ , and  $R_{27}...R_{34}$  should be bought

as ready-made networks, which are easier to handle Capacitors  $C_{9}$ .  $C_{9}$  are not yet fitted: more about this later. Connector  $K_{1}$  may also be omitted for the moment, as it is not required until the colour extension is added. Movable wire links AB, CA and IJ are best made with PCB pins and 2.54 mm matrix shorting sockets. Links KG, KH, E and F on the other hand, consist of stout wire or of normal soldering pins — see also Table 8.

Once this done, a first test should be made to verify that the supply voltage is present at the IC sockets or relevant soldering terminal on the board. Note that in the case of  $\mathbb{E}[\gamma_1, ... / \mathbb{E}_8]$  the +8 V line is connected to pin 8, and the return (earth) line to pins 1 and  $\mathbb{E}[\gamma_1, ... / \mathbb{E}_8]$ 

Next, oscillator  $IC_{28}$  and the address decoding ICs  $(IC_1...IC_3)$  should be fitted. When the supply is connected, pin 8 of  $IC_{28}$  should produce a clock signal of 12 MHz or 14 MHz, depending on the

When the decoding address for the graphics card is known, write to it the highest-value byte with the aid of  $S_1 \dots S_8$ . In case of address range EIXX<sub>bexx</sub> the situation shown in Fig. 19 then pertains.

As soon as an address from this range appears on the address bus, output P=O (pin 19) of IC; goes low. Pin 9 of IC; must be on at address XXSR while pin 10 should be active at address XXSR As these addresses are present on the bus of the microprocessor for very short times only, it is impossible to detect them with an oscilloscope 1 is.

When the address decoding has been tested in this way, buffer  $IC_0$ , and registers  $IC_0$ ,  $IC_{11}$ ,  $IC_{12}$ , and  $IC_{12}$  can be fitted. It is, of course, not permissible that these IC is affect the computer that controls the graphers card. Next,  $IC_{20}$ ,  $IC_{30}$ ,  $IC_{30}$  programmed PROM, and  $IC_{18}$  should be fitted. After this it should be verified whether signals STR, RAS, CAS, CK, LD, and ATX are present at

by P Lavigne & D Meyer



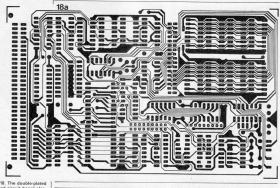


Fig. 18. The double-plated printed-circuit board also has through-plated holes.

Fig. 22. Wiring diagram for socket K1, which is required for the colour extension card.

Table 7. Order of actions and construction check

Table 8. Optional wire links.

Table 9. Memory ICs.

#### Table 9.

#### YES 2164-15 INTEL

(2164-20)

MSM3664-12 OKI MSM3664-15

MSM3664-20 MK4564-15 MOSTEK (MK4564-20) µPD4164-3 NEC

"PD4164.2) (uPD4164-1) HM4864-2 HITACHI

(HM4864-3) TMM4164P-2 TOSHIBA TMM4164P.3 (TMM4164P-4)

TMM4164AP-12 TOSHIBA TMM4164AP-15 (TMM4164AP-201

MB8264-15 FUJITSU

MSK4164ANP-15 MITSURISHI

NO	
MCM6664 MOTOROLA HYB4164 SIEMENS	
EF6665 THOMSON	

F4164 FAIRCHILD TMS4164 Texas Instruments IMS2600 INMOS

e /				

IC1...IC3 IC11...IC13

IC14;IC15 IC17...IC24

IC16:1C29:1C30

IC4...IC10;IC25;IC26

1Cs action(s) none carefully check the board optically and electrically none fit permanent wire links, 64-way plug, and IC sockets fit optional wire links (see Table 8), DIL switches none none test the supply voltages fit all resistors and capacitors, except C10...C18 IC<sub>28</sub>

fit an lesistors and capacitors, except C<sub>10</sub>...C<sub>18</sub> fit crystal X<sub>1</sub>; check operation of clock oscillator adress decoding XX50...XX5F and XX64...XX67 check signals STR; RAS; CAS; CK; LD; and A7X write 00...03, in that order, to address XX66 (IC11) and verify that the signal at A7X changes in accordance with Fig. 20 check signals ALL; BLK; ALLX; BLKX; DAD0...DAD6; SYNC fit capacitors C<sub>10</sub>...C<sub>17</sub>

check signals MUX; SH/I; RAS0 ... RAS7; I check the supply voltages and currents read XX50: 07, 05, or 0D

XX51: 00 XX53: 03 write XX64: 00

XX66: 00 (01, 02, or 03 for turning the page)

XX51: 01 XX53: 03

XX50: 0C (screen becomes white)

XX64: 01 XX50: 0C (screen goes black)

#### Table 8

Table

tick

positive sync pulse (HS+VS=CSYNC negative sync pulse (HS + VS = CSYNC) 4 pages; 256 lines; sequential scanning

2 pages; 512 lines; interlaced scanning 512 × 256 (EF9366); 512 × 512 (EF9365; EF9367) 512 × 256 (EF9367

EF9365:EF9366 | EF9367

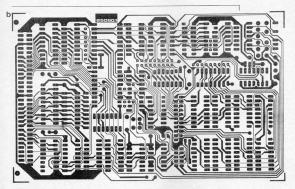
G:	yes	no
H:	no	yes
I:	no	yes
J:	yes	no

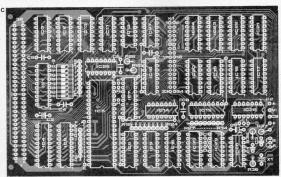
#### 22 K1

LPE	N	10	02	VIDI
EXT		30	04	MFREX
RMI	NS	50	06	DINX
CSY	NC	70	08	LD
GNE	)	90	O10	SH/L
GNO	)	110	O12	GND
WR:	₹	130	014	GND
RAS	6	150	O16	RAS7
RAS	4	170	Q18	RAS5
RAS	2	190	O20	RASE
RAS	3	210	O22	RAS1
A3		230	O24	Δ7

O26 AZ 270 028 AB 290 030 A5 CAS O32 310 BLKX WHI нск 330 024

1.50 elektor india january 1986





#### Parts list

R<sub>1</sub>...R<sub>15</sub>;R<sub>27</sub>...R<sub>37</sub>=1 k\* R<sub>16</sub>...R<sub>26</sub>=22 Ω

Capacitors: C<sub>1</sub> = 33 p

C2; C18" = 10 µ; 16 V C<sub>4</sub>...C<sub>8</sub> = 100 n C<sub>9</sub> = 1 μ;16 V tantalum C<sub>10</sub>...C<sub>17</sub> = 100 n\* IC1 = 74LS688 IC<sub>2</sub>;IC<sub>3</sub> = 74LS138 IC<sub>4</sub> = 74LS245 IC5 = EF9365; EF9366; EF9367\*

IC6;IC7 = 74LS374 IC8;IC9 = 74LS283 IC10; IC30 = 82S123 IC<sub>11</sub>;IC<sub>16</sub> = 74LS174 IC<sub>12</sub> = 74LS74 IC<sub>13</sub> = 74LS173 IC<sub>14</sub> = 74LS30 IC15 = 74LS166

IC<sub>17</sub>...IC<sub>24</sub> = 4164 (64 K×1 bit)\* IC<sub>25</sub>;IC<sub>26</sub> = 74LS32 IC<sub>27</sub> = 74LS08 IC<sub>28</sub> = 74LS04 IC29 = 74LS191

Miscellaneous:

X<sub>1</sub> = crystal 14 MHz (EF9365; EF9366) or 12 MHz (EF9367) in HC18U or HC25U case K<sub>1</sub> = test socket; double row; 17 way; matrix

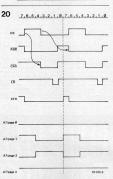
2.54 mm for mating with flat ribbon plug K<sub>2</sub>=4 small soldering pins S<sub>1</sub>...S<sub>8</sub> = octal DIL switch in 16-way case 6 standard soldering pins

three 3-pin test plugs with suitable short-circuit sockets PCB 85080-1

\*see text

Fig. 19. Example of setting of DIL switches S<sub>1</sub>...S<sub>8</sub>; here set for address decoding at \$E1XX.

Fig. 20. Timing diagram of a number of the more important signals. The chronological order of these is also important. Note that the four signals Ay do not appear simultaneously, since each belongs to a different page. These signals are produced by writing data 0, 1, 2, or 3 to XX66hex in register ICI<sub>3</sub>.



X

the output of  $IC_{16}$  and the inputs of the relevant ICs — particularly pin 1 of  $IC_5$  (which has not yet been fitted). The timing diagram of these signals is given in Fig. 20 (see also Fig. 16 in the November issue).

The card is now in such a stage that the most important components can be fitted: graphics display processor  $IC_5$  and memories  $IC_{17} \dots IC_{24}$ , as well as all the other ICs, such as PROM  $IC_{10}$ .

Before any further progress can be made, a monitor is needed. If this has a separate SYNC (link A) or SYNC (link B) input, carry on. If, however, it needs a composite video signal, it is necessary to first build the mixer stage of Fig. 21.

When the supply is switched on (power-on reset) nothing will initially be visible on the screen. Decause the image depends, among others, on the decoupling capacitors on the power lines to the dynamic RAMs. These capacitors,  $Cg_0$ ,  $Cg_1$  are not fitted on the component side of the board, but direct at the supply connections (plus 8 and 16) of the ICs as shown in Photograph I. It is very important that the leads of these capacitors are properly insulated. Without these capacitors are properly insulated. Without these capacitors are properly insulated. Without are so typically produced by the RAMs.

There are, therefore, sound reasons for fit-

ting the capacitors at the track side of the board.

When, after the capacitors have been soldered in place, no image at all appears on the screen, this is normal. It may also be that there are some vertical lines visible. All that is not so important; what is, however, is that the screen image does not change after the power has been switched on. It is also advisable to check the supply to the control computer before and after the graphics should be of the order of 450 mA, but may vary widely from this figure, depending on the screen image. The only IC that may get slightly warm is the GDP all the others should stay fairly cool.

When power is switched on, the registers of the GDP may be read; they should show: XX56: 07, 05, or 0D

XX51: 00

XX53: 03 in hexadecimal, of course.

It is now possible to carry out a simple test:

• enable the write mode of the screen memory by writing 00 to addresses XX66

and XX64.

Next, write @C to XX50 in the command register of the GDP.

 If the contents of that register is Ø3 at XX53, the screen will go white.

AXSS, the screen win go writing 81 to XXS4 and giving the GDP the command 8C at XXS8 If wereything is in order, the screen should now turn black. Note that the command 8C at XXS8 cannot be read, since at this address of the graphics processor writing accesses the command register, and reading, the status register.

If nothing happens on the screen, either the graphics processor has not received any instructions, or the logic levels on the WRIS and DIS lines were not correct for a write operation. In either case, the WRIS line must be logic 0 to enable the memories to be accessed, while DIS must be logic low to light the pixels, and logic 1 to quench them. It may be that the output-register has not received signals HCK and SH/I required for its proper operation. It is also necessary to verify signal STR at IC7, signal RAS for collective accessing at pin 14 of IC10, and signals RAS for individual accessing at pins 1...7 and 9 of IC10. The slightest shortcircuit or bad contact at one of the signal lines can disrupt or even disable the whole system.

If acmeshing has gone woong, a systematic search and verification of the various signals will soon show where the fault lies. For instance, it signal CK is not present at join of  $U_0$ , the device will not work. The same applies it signals RRS, CRS, or HCK are represent. If one of the signals RRS, CRS, or HCK are not function; for the signals RRS, CRS, and the continuation of the signals RRS, and the signal continuation of the properties normally. If one of the DAD lines is short circulate, the card will only function partly; the extent of the malfunction depends on the binary loading of the relevant address

The information given here, particularly that in Table 7, will enable anyone with some experience in electronic construction to build the card satisfactority. The colour extension will be dealt with in a forthcoming article. Remember that plug K, will provide the connection between the black-andwhite card and the colour extension. The wiring to this connector is shown in Fig. 22. Until the colour extension is there, the plug is useful in providing test signals.

## Choosing the GDP and memory ICs

As the EF9368, EF9366, and EF9367 cost roughly the same, it is best to buy the EF9367. This is the latest model and also the most efficient; moreover, it can be used in interfaced as well as in sequential scanning. In the present graphics card, it permits the following modes of operation:

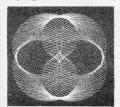
512×256 (sequential scanning), and 512×512 (interlaced scanning)

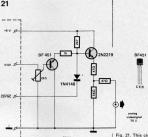
It also offers the possibility of providing 1024 x512 pixels, but this is not used in the card, mainly because this mode of operation demands a very high quality monitor, particularly as regards bandwidth and resolution.

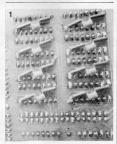
The EF9366 is also an excellent device, but it cannot work in interlaced scanning mode and its resolution is, therefore, limited to 512 x286 pixels in the present card. In most cases, this is, however, perfectly satisfactory. It should be noted in this context that a vertical resolution of 512 pixels in interlaced mode requires a good-quality colour monitor.

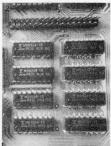
Choosing the correct memory ICs is also important. In theory, they should be fast, say, 150 ns access time or better, so that the RMW mode functions properly. Practical experience with the GDP has shown, however, that in many instances access times of 200 ns or even 300 ns do not pose particular problems.

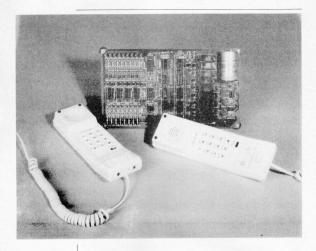
Moreover, these ICs do not get a refresh pulse on  $F_3$  and  $F_{\rm BB}$  and pin | (which is earthed) does not receive a refresh clock pulse. Table 9 shows all suitable types as far as they are known to us (column YES); those in column Nor are definitely NOT suitable. The types in brackets in the YES column have an access inten that, theoretically, is too of the Japanese firm Pulsua as used in one of the planeses of the Pulsua as used in one of the planeses.











# telephone exchange

by J Steeman

Nowadays, there is a variety of inexpensive, yet sophisticated, telephone sets on the market. Not all of these are permitted to be connected to the British Telecom network, however. None the less, two or more of such sets may be used to form a simple, but effective, internal telephone system for the home, an office, or anywhere where a number of people want to communicate from different locations within the same building.

The proposed system may, of course, also be built around British Telecom approved sets. Note that the system is intended for up to eight sets each of which generates a pulse code when a number is dialled or keyed in.

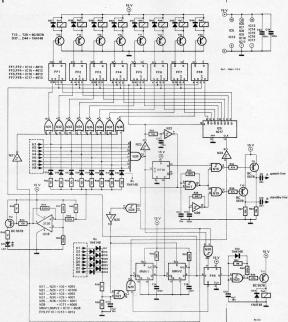
#### **Facilities**

A telephone exchange does, of course, more than just connecting one set to another. In fact, this is about the only thing it need not do, because the set with which communication is required is already accessed by the pulses generated when the relevant number is dialled or keyed in. What the exchange is required to do is:

- to decode and process the pulses generated by the telephone sets:
- eto generate a dialling tone:
- to generate a diaming tone;
   to generate and pass on a ringing tone;
- to interconnect sets as soon as the receiver is lifted;
- to prevent a third set listening in;
- to prevent a third set instending in,
   to generate and pass on to a third set an engaged tone.

In addition, the system allows communication between two sets to be established in two ways:

- by dialling or keying in the required number and waiting till the other set responds;
- · semi-automatic: when the receiver of one



set is off the hook, and the receiver of another set is lifted, the two sets are interconnected, even when no number has been dialled.

The exchange is provided with LEDs that show at all times which of the sets; if any, are engaged. A ninth LED indicates whether the exchange is engaged or not this only goes out when the communication has been terminated, i.e. when the two relevant receivers have been replaced on their restations and the standard of the restandard of

Calling one set from another is done by simply dialling or keying in the number of the wanted set, i.e. 1...8.

#### Circuit description

Since the telephone sets can only be connected to the exchange  $\sim$  see Fig. 1 – via nected to the exchange  $\sim$  see Fig. 1 – via the interface shown in Fig. 2, it is important to know how many sets the exchange will control before all the parts are bought. If, for example, only three sets are envisaged, the relevant part of the circuit in Fig. 2 needs to be built only three times. If, however, the full capacity of the exchange is used, eight interfaces are required.

As soon as the receiver of a set, say, number

Fig. 1. Circuit diagram of the home telephone exchange.

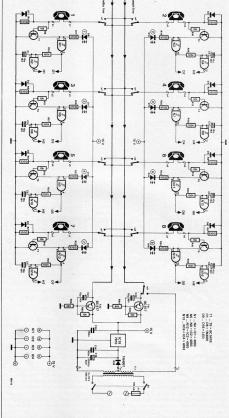
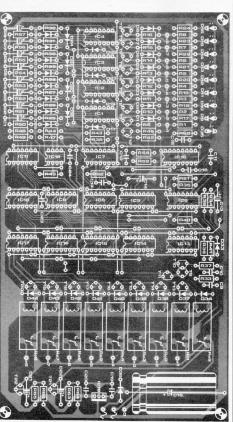


Fig. 2. Circuit diagram of the power supply (for up to eight telephone sets) and the interface required for connecting the telephone to the exchange. One interface is required for each and every telephone.



Parts list

R<sub>1</sub>...R<sub>8</sub> = 330Ω R37; R43; R49; R50... R57; R50;

R<sub>25</sub>...R<sub>32</sub>;R<sub>40</sub>;R<sub>46</sub>...R<sub>48</sub>. R<sub>61</sub> = 100 k R<sub>35</sub> = 180 k R<sub>38</sub> = 4M7

R<sub>41</sub> = 1 M R<sub>42</sub>; R<sub>45</sub>; R<sub>58</sub> = 1 k R44 = 820 k Ren = 10 M R<sub>64</sub>; R<sub>66</sub> = 68 k

C1...C8; C21; C22 = 10 µ; 16 V

C<sub>24</sub> = 100 n C<sub>13</sub> = 4µ7;16 V C<sub>18</sub> = 2200 µ;40 V Semiconductors:

Tg = BC547B T<sub>21</sub>; T<sub>22</sub> = BC557B T<sub>21</sub>; T<sub>22</sub> = BC517 D<sub>1</sub> . . . D<sub>8</sub>; D<sub>36</sub> = 1N4001 D<sub>9</sub> . . . D<sub>16</sub>; D<sub>35</sub> = LED Day ... Dag = 1N4148 IC1...IC4;IC6;IC8 = 4093

IC<sub>5</sub> = 4017 IC7 = 40106 IC9;IC10 = 4001 IC<sub>11</sub> = 4068 IC13...IC17 = 4013

IC<sub>19</sub> = 7815

Miscellaneous: S<sub>1</sub> = double pole mains switch

Re1...Re9 = 12 V relay for PCB mounting (e.g., V23027-B0002 - may be (0784) 33603 or (061 432) 4945)

Tr1 = mains transformer; 2×18 V at 500 mA; 0-18-36 V at 500 mA; or 0-18 V at 500 mA + one of 0-36 V at 50 mA 1 = fuse, 100 mA

Fig. 3. Printed circuit board of the telephone exchange. Note that the mains transformer is not fitted on this board. The track side of the PCB is shown on page 45.

l, is taken off the rest, the relevant transistor, here  $T_1$ , is switched on, so that the output of  $N_1$  goes high. After a time determined by the RC network at the input of  $N_2$  this gate toggles and its output goes low.

If now from set 1 a number is dialled, the output of  $N_i$  will toggle in rhythm with the pulses produced by the telephone set. Because of the RC network at its input, gate  $N_2$  will not follow suit: its output remains logic low during the dialling of a number. The low logic level indicates set in the control of companion ( $N_i$ ) consists in taken off its rest, the output of companion ( $N_i$ ) goes low, which renders all other sets inoperative. How this is achieved will be reverted to later.

The pulses generated during the dialling of a number trigger monoflops MMV, and MMV2 via one of the lines D1 ... D8, and also serve as clock signal for ICs, a counter with ten outputs. The contents of this counter, i.e., the dialled number, is only accepted by bistables FF1 ... FF9 if two conditions are met: (a) only one receiver is off its rest, and (b) FF, is not generating a ringing tone. As long as pulses keep arriving at pin Il of MMV, the Q output of this monostable will remain high. When this pulse train comes to an end, a short pulse is provided at the Q output (pin 6) of MMV2. This pulse sets FF (which generates the ringing tone) and clocks bistables  $FF_1 \dots FF_8$ , depending on the output code of ICs. The wanted set is then connected to the speech line via its associated relay. At the same time,  $N_{20}$  (an oscillator with a long 'l' and a short '0') intermittently connects the bell voltage onto the speech line via contact reg (see Fig. 2). The wanted telephone will then ring until its receiver is lifted.

To ensure that a third set cannol listen int. the logic levels at the Q outputs of  $F_1$ ...  $F_2$  are held: this is done by making both the set and reset inputs of these bistables low when the receivers of two telephone sets are of their rests. The set inputs are made low via  $C_{12}$ : the output of this opamp is low when two telephones are interconnected. The output of Schmitt trigger  $H_2$  is then shalf,  $F_2$ ...  $F_3$ , and  $H_3$  is then shalf,  $H_3$ . The set inputs are not set inputs of the bistables are low. As long as two receivers are off the hock. the output  $G_{23}$  is logic high. The output  $G_{23}$  is logic high.

bistables  $FF_1 \dots FF_8$ , is then low. An engaged tone, generated by gates  $N_{19}$ and  $N_{26}$  in combination with transistor  $T_{10}$ , and applied to the wait line, indicates that the exchange is busy. This tone generator, as well as the dialling tone generator consisting of  $N_{17}$ ,  $N_{18}$ , and  $T_9$ , is actuated by FF10 as soon as the receiver of any one of the sets is lifted. The dialling tone generator is provided to indicate that the exchange is processing a number: is has no connection with the actual dialling pulses. In fact, as soon as a dialling pulse appears on one of the D lines, the dialling tone generator is switched off immediately by FF Semi-automatic operation is achieved as

follows. As stated, bistables  $FF_1$ ... $FF_8$  are rendered inoperative when two telephone sets are communicating. When only one receiver is off the hook, the output of  $N_8$  is low, and the bistables can still be accessed. When in that condition a second receiver is lifted, it takes a second before the bistables are really inoperative, and the two telephones are innerconnected. Note that it elephones are innerconnected. Note that it only the control of the condition of the con

#### Power supply

The +15 V power supply is provided to the exchange via the speech and standby lines. The bell voltage — here chosen at  $2 \times 18 \text{ V}$ — is also applied to the speech line, but in this case via relay  $Re_g$ . Transistors  $T_{21}$  and  $T_{22}$  ensure a high supply impedance to prevent attenuation of the speech signal.

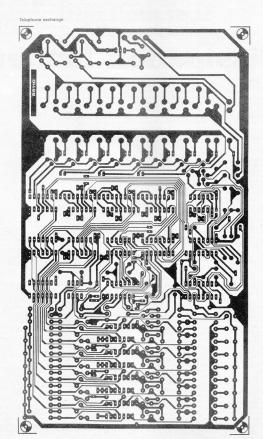
#### Construction

As no presetting or alignment is necessary, the exchange may be fitted in a suitable enclosure as soon as the wiring of the PCB shown in Fig. 3 has been completed. The telephone sets are connected to terminals a, ...a, and b,...b, on the board respectively. Note that British Telecom approved sets need a four-wire connection to the exchange, because their bell circuits need to be connected separately to the standby and speech lines respectively. The bell writes in these sets are coloured red and green, while the other two are blue and white respectively.

#### Finally

Because of the RC network between gates  $N_1$  and  $N_2$  (or  $N_{257}$  and  $N_{4,60}$ ) in the interface circuit of Fig. 2, the bell things briefly when the wanted extension picks up the receiver. This could have been eradicated, but it was not thought that the cost of the additional electronics required was justified by this very minor flaw.





In a stabilized power supply the dissipation in the stabilizer may become very high when the difference between input and output voltages is large. This phenomenon occurs particularly in stabilized mains supplies, and can be remedied by lowering the secondary voltage before the stabilizer. The suggested circuit here does this in a neat manner by making it possible to select either the full or half the secondary voltage. And that with only a few components!

# dissipation limiter

switches transformer secondary

Z. Paškvan

Figure 1. Switching from half-wave to full-wave rectification is effected by applying a suitable voltage to the base of T2.

Figure 2. This diagram shows the effect on the voltage applied to the rectifier circuit when a base voltage, U<sub>B</sub>, is applied to T2.

Figure 3. To get the full secondary output voltage, a voltage of 1...10 V must be applied to the base of T2. Rectifiers D1, D2, Th1, and Th2 are then connected in a bridge configuration.

Figure 4. When Th1 and Th2 are off, D1 and D2 function as half-wave rectifiers. As the two halves of the transformer secondary are effectively connected in parallel, the pulsating voltage applied to the rectifiers is  $\frac{1}{2}$  U<sub>2</sub>.

The stabilizer in a power supply may get usery hoi indeed when the output oursent is high and the output voltage is low, because it alone has to dissipate the difference between the input and output voltages. It should therefore not come as a surprise when the device gives up the ghost. Some supplies are fitted with a switch that enables the lowering of the switch that enables the lowering of the stabilizer need not dissipate so much power.

The proposed circuit shown in figure 1 also makes it possible to lower the secondary transformer voltage but in a rather special way. With a centre-tapped transformer it is possible to halve the output voltage, u.g. by switching the rectifiers from full-wave to half-wave rectification. The switching is effected without the use of a mechanical switch: all that needs to be done is to give the correct instruction and even that could be automated.

## From full-wave to half-wave and vice versa

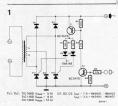
The two parts of the secondary winding of the transformer must be in series to give us = u1 + u2. All that is necessary to ensure this is to apply a voltage of 1...10 V to the base of T2. Both that transistor and Tl then conduct: siliconcontrolled rectifiers (SCRs) Thl and Th2 are consequently switched on via R1, R1', D4, and D5. The SCRs together with D1 and D2 now form the wanted full-wave rectifier in a simple Graetz circuit as shown in figure 3. Diode D3 ensures that in this configuration the secondary windings are not short-circuited via the centre tap and one of the SCRs. When it is required to halve u, the base

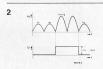
tap and one of the SCRs. When it is required to halve u<sub>s</sub>, the base voltage of T2 should be made zero. The SCRs are then off and rectification is carried out by D1 and D2 only; this is half-wave rectification. This situation is shown in figure 4 and illustrated in figure 2. You will see that the peak value of u<sub>s</sub> tiese appreciably when a base voltage is appried to T2, and drops to u<sub>1</sub> = u<sub>2</sub> when the base voltage is removed. No values have been given in this article because these will of necessity depend on the type of supply used. All compo-

nent ratings, particularly those of DI and

D2, must, of course be chosen to comply

with your particular requirements.









## **Digi-Course II**

Chapter - 2

In the last chapter of Digi-Course II we have seen how two NAND gates can be connected to make a simple R-S Flipflop.

simple R-S Flipflop.
The circuit and its truth table is again reproduced

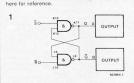


Table 1	S	R	Q	Q
	0	0	1	1
	0	1	1	0
	1	0	0	1
	1	1	0.1	1/0

As we have already seen, the last line of the truth table is ambiguous. Its relation is not defined in isolation, but the previous history of the outputs is involved in deciding which of the outputs will be and which will be 0. The gate which had a "O" input prior to going on "I". The same a "I" at the actious Libburger of the properties of the propert

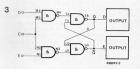
Our R-S Flipflop works as an impartial judge of this game. The basic idea is very simple. A metalic iring is passed over a complexly bent wire such that the ring surrounds the wire but does not touch the wire at any place. If the ring touches the bent wire at any moment, that player has to drop out. Whether the ring touches the wire or not, is faithfully recorded by the R-S Flipflop.

The bent wire is connected to the ground terminal on the Digilex board and the ring is connected to the input S (Pin K13) At the beginning of every round, the Ringut (Pin L 10) is momentarily shorted to the ground terminal. This gives "O" input to R, thus resetting the Flipflop, and turning off the O output LED. Now the player engages the ring abound the bent wire at one end and starts moving it towards the other end, without touching the wire with the ring, a second, the Flipflop is immediately set and the O output LED glows, announcing the player to be an "Old Shatter hand".

The simple Flipflop circuit that we have just used can be said to have stored the information that the particular player has touched the wire with the ring. In short, the Flipflop is a memory device which stores the information last received. The information stored is always in form of zeroes and ones.

For complex memory applications like data storage, data processing, calculations which require large quantities of data to be handled, we require extremely large quantities of storage cells like Flipflops. Each Flipflops common the process of the flipflops of the flipflops of the flipflops common the flipflops. These Flipflops are internally arranged in such a way that they can be accessed externally using just a few pins. (16 thousand prise are not required!)

Nowlet us see another variation of our basic Flipflop. Connect two more NAND gates as shown in figure 3.



This arrangement is called a controlled or clocked R-S Flipflop. The Flipflop of this circuit can change its state only when a "1" appears on the control or clock input C.

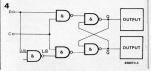
Table 2.	0	0	0	0/1	1/0
	0	1	0	0/1	1/0
	1	0	0	0/1	1/0
	1	1	0	0/1	1/0
	0	0	1	0/1	1/0
	0	1	1	0	1
	1	0	1	1	0

D E | C | Q Q

The last condition which was present when C changed to "1" is retained, depending on which input had a "0" at that moment.

The C input thus behaves like a "Store" command input. The input conditions present when this input gets the "Store" command (logical "1" on C) are allowed to set or reset the Flipflop and this condition at the output is retained till a new "Store" command appears at pin C.

One disadvantage that still remains in this circuit can be removed by modifying it as shown in figure 4. Now we have only one input D for Set/Reset and one input for the control command "Store".



## selex

The restriction of having to use a zero as an input on  $\overline{R}$  and  $\overline{S}$  is now removed. The pin D can accept either zero or one.

Table 3

D	C	Q	Č
1	1	1	C
0	1	0	1
1	0	0/1	1,
0	0	0/1	1

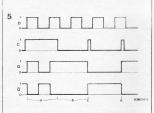
Depending on the last condition when a "1" appeared on C

The functioning of this circuit can be summed up briefly as follows.

"1" appearing on D sets the Q output to "1"

"O" appearing on D resets the Q output to "O" provided a '71" was present on C. When a "O" is present on C, input D becomes ineffective.

As we have already seen, truth tables in case of Flipflops have ambiguous entires due to the time dependent nature of Flipflop operation. A better way to understand Flipflop operation is the use of timing diagrams.



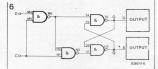
These timing diagrams clearly illustrate the following properties of the Flipflop.

 When C<sup>±</sup>1 (section a), whatever appears on D also appears on output Ω.

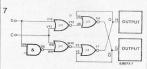
2. When C-O (section b), output retains its last level when C changed from 1 to 0 which means that the condition of output Q which was present at the moment when C changed from 1 to 0, is stored in the Flipflop. This is called the falling edge of the input on C.

Now that we have seen that only the following edge of input on C is effective as a "Store" command, we can easily understand the significance of points C and in the timing diagrams.

How long the level at c remains "1" is not important, what is important is only the moment it falls to "0" whatever state Q has at this moment is then retained. The circuit of figure 4 can be further simplified as shown in figure 6.

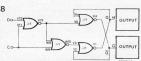


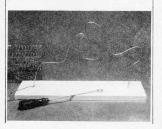
And using the counterpart of the NAND gate, namely the NOR gate, we can construct a Flipflop as shown in figure 7 below.



The NOR-Flipflop functions a bit differently from the NAND-Flipflop. Now that you have studied the NAND Flipflop in detail, it will not be difficult for you to prepare the truth table for the NOR-Flipflop as well, alongwith the timing diagrams!

A simplification of the circuit of figure 7 is also possible, and it is given below for reference.





"....... do you know different word for resistors?"
"No."

"Semiconductors!"

"Why do you say that?

**Semiconductors** 

Resistors are not semiconductors"

"Why not? If something has almost no resistance, we call it a conductor. If something has such a high

call it a conductor. If something has such a high resistance that it does not allow any flow of current, we call it a non-conductor. So if something has a resistance in between these two we must call it a semi-conductor!"

"No, it is not so. The resistors are not semiconductors. The word semiconductor has a different meaning.

"Semiconductors are materials which sometimes conduct and sometimes don't. When they conduct and how much they conduct depends on various other factors."

"Then these semi conductors must be some sort of switches!

"No, these are not switches either, you are somewhere near the truth. We shall see a semiconductor with an example. You have seen a dioded this is a semiconductor. It has two leads, and its symbol is like an arrow head and a bar. A current can flow through the diode if it has a direction which coincides with that of the arrow. In the other direction, no current can flow.



For simplicity let us say that when the plus pole of the applied voltage is behind the arrow, a current can flow, however when the plus pole is in front of the arrow, the bar between the plus pole and the arrow head can be imagined as blocking the current flow," "In that case, we can call the diode as an electrical

'One Way street!"
"Exactly that is what a diode is!

It acts as a conductor for current trying to flow in the direction of the arrow and it acts as a non-conductor for current trying to flow in the opposite direction.

This is the reason why it is called a semi conductor. We can also compare the diode with a unidirectional valve which opends only in one direction".

"But how do you empare this with a switch?"
"Let us again take the example of a diode.



This illustration shows a diode connected in series with a lamp and a battery. As we have already seen, the diode will allow the flow of current because the place pole is behind the arrow head. The current will flow through the lamp and the lamp will glow. This diode is acting as a closed switch between the battery and the lamp.

Let us see now, what happends when the battery polarity is reversed



This time the plus ple is in front of the arrowhead and the bar comes between them. The current flow will be blocked by the bar and the lamp will not glow. The diode now behaves as an open switch."

"Then this circuit can be used as a polarity tester?"

"Right! The diode can certainly be used to check the polarity, because it behaves as a closed switch with the correct polarity and as a open switch with the wrong polarity"

"Is that all a diode can do?"

"Of course not! The diode is a very versatile device. Haven't you seen these diodes in a battery eliminator? There they work even four at a time."

"What are these diodes doing in a battery eliminator?"

"Diodes are used to rectify the alternating voltage. We have already seen how alternating voltage behaves, haven't we?"

"Yes, I remember quite clearlly. The alternating voltage changes its polarity hundred times every second. Therefore on every terminal of the plug socket there is always a plus pole and a minus pole alternately ......"

"...... and on the other terminal it is the opposite, i.e. minus pole and plus pole".

"That is quite logical!"

"And for obtaining a direct voltage from an alternating voltage, we put a diode in the circuit. It allows the current to flow when the polarity is correct. It blocks the current flow when the polarity is wrong. As the diode allows only half the cycle of the alternating current to pass through, it is called a half wave rectifier.



### selex

## **Transistors**

" What are transistors ?"

"Transistors are also semiconductor devices like diodes. We have seen how diodes function like one way valves. Transistors also behave like valves, but with a difference. Transistors are valves for electrical currents which can be regulated. With transistors, we can make currents flow with greater or lesser intensity."

"They are the water taps of electronics"

"Right !"

"But then the current can also be regulated with a potentiometer."

"A potentiometer has to be turned mechanically. It has a rotating spindle and a knob. The transistor has no such mechanical parts, it functions fully electronically. Most of the transistors are quite small and they have three leads coming out at the bottom."

"Three terminals? Then certainly it has some kind of a potentiometer inside?"
"No, transistors and potentiometers have absolutely

"No, transistors and potentiometers have absolutely nothing in common. The transistors have two very intelligently designed diodes inside them, as you can see in the illustration given here."



"How can you control current with two diodes?"

"No, it is not at all possible just with two diodes. But it can be done with a transistor. The illustration we just saw is nothing but a simplified view and not an physical combination of two ordinary diodes"

"I don't understand!"

"This is how it happens: When a positive voltage is applied to the base, a cuttent flows through the diode between the Base and the Emitter."

#### (Illustration)

"Now, if we apply a negative voltage to the Collector current is also allowed to flow through the diode between the Base and Collector".

#### (Illustration)

"That is right, and with a plus pole connected to the collector....."

"..... the upper diode will block the current! What is the use of all this?"

"Just wait. It is certainly true that the upper diode will block. But as I have already said, these are just ordinary diodes. These diodes are designed in such a way, that the upper diode becomes conductive due to the current flowing through the lower diode. To

simplify the matters we can say that the current flowing into the Base terminal, takes along a current from the collector and comes out through the Emitter terminal."



"Unbelievable! Do you mean to say that a current flowing into the Base can make the upper diode conduct a current from Collector to Emitter even with a plus pole connected to the collector?"

"That is exactly what happens inside a transistor. However when there is no Base current, the upper diode can block the current and no current can flow from Collector to Emitter."

"This means that the transistor is woking like a remote control switch, which is switched ON and OFF by the current flowing through the Base."

"Yes, it can be used as a switch controlled by the Base current. Incidentaly, as the transistor is not just two diodes connected together, it has been given a different symbol"



"Does the transistor work only like a switch? Then what does it do in an amplifier?"

"The Collector current is not only switched ON and OFF by the Base current. It can also be regulated by the base current. As in case of our water rap, the quantity that can flow through its adjustable. The Collector current can be as strong as 500 times the Base current. A Base current of just two mannaperes can cause a Collector current of about in electronic circuits."

"This means that a transistors can also be called an amplifier?"

"Yes, and the ratio of the Collector current and the Base current can be said to be the amplification factor of that transistor"

"Do the Hi-Fi Stereos also function in the same manner?"

"You are right! However, a large number of transistors and many other components are necessary to obtain the Hi-Fi quality Stereo amplification."

"Has nobody ever thought of building an amplifier from water taps? We could call it an Under-water Hi-

1.64 elektor india january 1986

# Resistance Bridges

In electronics, circuits similar to that shown in figure 1, are called bridges. When all the elements of such a circuit are rectifier diodes, we call it a rectifier bridge - which is commonly used in battery eliminators. When all the elements of such a circuit are resistors, we call it a resistance bridge. The circuit of figure 1 is also known as a "Wheatstone Bridge."

Now, let us see the functioning of this bridge circuit, frame in a simplified manner for better understanding. The two simplified manner for better understanding. The two branches made up of R. R., and R. R. are nothing but voltage dividers. The meter connected between these two branches at the junctions between R. R. and R. R. measures the difference between the two junction points.

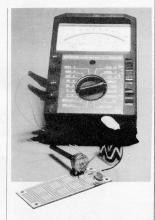
In the circuit of figure 2, the lift branch R<sub>1</sub>, R<sub>2</sub> divides the voltage into 2V on R1 and 3V on R2. The right branch divides the 5V supply voltage into 2.5V and 2.5V across R<sub>1</sub> & R<sub>4</sub>.

The difference voltage thus becomes 3V - 2-5 V = 0.5 V which is measured by the meter and indicated

If we now reduce R1 to  $260\Omega$  instead of  $400\Omega$ , the voltage across R2 will increase by about 0.5V. This will increase the reading on the meter across the junction points by 0.5V. The meter connected directly across R2 will also show an increase in reading by 0.5V.

A very important fact can be observed here that for a rise of about 17% in the voltage across R2 then has been a rise of 100% in the difference voltage across he junction points of the bridge, which can also be called as the bridge voltage. The above observation shows how sensitive the bridge voltage is, in relation to any change in the individual branch voltages. Even the slightest change in branch voltages will be reflected as a large deviation in the beidge voltage. When the ratios of the voltage divider branches on left and right are both exactly identical, the bridge voltage in zero.

This condition of the bridge when the voltage divider ratios of both the branches are identical is known as the balanced condition - the bridge is said to be a balanced bridge.



A simple applications of this bridge circuit is shown in figure 3, which works as a light intensity meter. One of the resistors in the left branch of this bridge is an LDR. The Resistance of an LDR in darkness were high, whereas its resistance in light falls as the very high, whereas its resistance in light falls as the ST of the second of the second







## selex

the meter reading becomes zero. If the potentiometer knob is provided with a dial, it can be calibrated to read the light intensity.

This circuit can work even without the resistance R2  $(1K\Omega)$  shown in dotted lines, but to protect the LDR from excess current when the potentiometer is in the extreme lower position, R2 should be included in the circuit.

The potentiometer position required, to obtain zero reading on the meter when the LDR is in total darkness, can be marked as zero intensity. The light can then be increased in known steps of intensity level and every time the position of potentiometer can be marked with the known value.

This circuit can be easily assembled on a small SELEX board and calibrated using a standard light intensity meter as a reference.

For a resistance bridge, the individual values of the resistances are not important. The operation depends only on the resistance ratios of the individual branches. When the resistance ratios of the left and right branch become equial, the bridge voltage becomes zero, irrespective of the individual values of resistors and supply voltage.

Figure 1:

Standard configuration of a bridge circuit. The individual elements can be any type of impedances, or even rectifier diodes.

Figure 2:

The meter connected across the junction points measures the voltage difference between those two points. The sensitivity of this measurement is much more than that of a meter directly connected across one of the voltage divider resistor.

Figure 3:

A simple light intensity meter. The potentiometer is used to balance the bridge in such a manner that the voltage across the shunt arm of the bridge in is zero.

Figure 4:

The circuit of figure 3 can be assembled on a small SELEX PCB.

## Resistance Decade Box



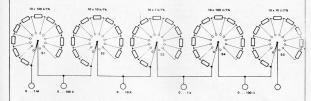
Even though the resistance decade is not a very sophiticated circuit, it has very great practical utility. The resistance decade box can be used for experiments, testing, bridge balanceing, voltage dividing and many other priactical applications. The values available being adjustable from 100 to more than 1M0.

The circuit is shown in figure 2. This is a selectable series connection of total 50 resistors, Individual resistors are added in to the series combination by selecting the switch positions. Each switch selects the number of resistors from a group of 10 resistors to fequal value. The total resistance of equal value. The total resistance of equal value.

comprise of effectively a discovering an acceptance of the selected by the 5 switches, as shown in figure 3. The individual values of resistors used in each group are selected in such a way that each switch represent a decimal place in the final effective combination of the five groups.

The series combination of five groups R1 ...... R5, added up to make the total resistance Rg = R1 + R2 + R3 + R4 + R5, lies between the external sockets A and F of the decade box. It is not essential that sockets A &r must be used as the two ends of the effective resistance from the decade box. Any pair of

2



sockets can be used for this purpose, depending on the required resistance value, as shown below : R (A-C) = R1 + R2

R (A-C) = R1 + R2 R (B-D) = R2+R3 R (C-F)= R3+R4+R5

As the 5 groups of resistors covered by the 5 switches have 10 resistors of equal values each - the switch position multiplied by the individual resistor value gives the effective values of R1 ...... R5.

For example, switch 1 controls a group of 10 resistors of 100KΩ each. Thus the switch 1 set to position 3 will give a value of 300KΩ for R1. Switch 1 set to positions 0 to 10 will give values of 0 to 10 MΩ is steps of 100 K  $\alpha$  each.

The individual values within each group are selected as multiples of 10. Group 1 has 10 resistors of 100K1 each, group 2 has 10 resistors of 10K10 each and so on, group 5 has 10 resistors of  $10\Omega$  each. Advantage of this selection of values is that each switch represents one decimal place.

Every switch is connected with an external socket, up the required value of resistance. Using all five decimal places may turn out to be ineffective due to the individual tolerance values of resistors. For instance, let us examine a setting of switches in the sequence 51381 (5138101). As we have used resistors with 1% tolerance, the error on the 1st switch itself can build up to 50 (1% of 500k1) thus which itself can build up to 50 (1% of 500k1) thus provided with five switches so that the full range of values is a valued to the second to the

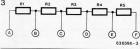
#### Construction Details:

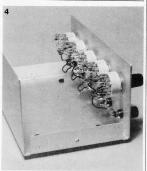
The construction of resistance decade box mainly consists of soldering work and mechanical fitting work. The 50 individual resistors must be soldered onto the lugs of the 5 different switches as shown in figure 6. The final fitting into a standard enclosure is simple, as shown in figure 4.

1% Metal Film Resistors must be used if a good accuracy is to be achieved. Even 5% Carbon Film Resistors will work equally well if the accuracy aspect is neglected.

#### Applications :

This resistance decade box can be used as an adjustable resistor, or as a voltage divider. When using this as a adjustable resistance, only 3 digits (switches) can be used at a time due to the accuracny consideration we have already seen. The remaining two switches can either be set to zero or





## selex

we can use only the sockets relating to the switches of interest.

When using the circuit as a voltage divider, any one of the sockets B, C, D, E can be used as the output socket for the divided voltage and the switches can be set to suitable positions to achieve the desired voltage division.

A more useful application of this circuit is as a decimal voltage divider. For this the switch settings must be in the sequence 9, 9, 9, 9, 10 on the five switches from left to right. The effective distribution of resistors is as shown in figure 5. By concetting a voltage V across sockets A and F we are able to get the following voltage outputs on the socket.

A = V

B = V/10 C = V/100

D = V/1000 E = V/10000 F = 0

This has been achieved by the fact that the resistance between the pairs of sockets are distributed as follows.

R (A-F) = 1 M $\Omega$  = Rg R (B-F) = 100 K $\Omega$  = Rg/10

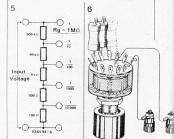
R (C-F)= 100 K $\Omega$  = Rg/100 R (C-F)= 10 K $\Omega$  = Rg/1000 R (D-F)= 1 K $\Omega$  = Rg/1000

R (E-F) = 100 Ω = Rg/10000

Using this circuit it is possible to obtain very small voltages. For instance, a 4.5V battery connected across sockets A and F will give a voltage of 450 micro volts at socket E.

#### Figure 1 :

A good sturdy sheet metal enclosure gives a professional appearance to the decade box. The front panel graphics has been designed for ease of understanding the operation.



#### Figure 2:

The resistance decade contains 50 resistors. They are divided into 5 groups. Each group has one selector switch for selecting the individual digit value of that decimal place.

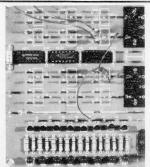
#### Figure 3 :

The effective combination of the resistances. R1, R2 ..... R5 are selected by the switch positions. A,B,C ..... F are the sockets on the front panel. Figure 4:

Inside view of the assembled decade box. The switches are mounted on front panel.

Figure 5

Use of the resistance decade box as a decimal voltage divider. Voltage divisions available are from 1/10 to 1/1000



# The Digilex-PCB is now available!

The Digilex-PCB is made from best quality Glass-Epoxy laminate and the tracks are bright tin plated, the track side is also soldermasked after plating. Block schematic layout of components and terminals is printed on the component side.

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## new products

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For further information, contact: Indian Engineering Company Post Box No. 16551, Worli Naka, Bombay - 400 018.

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## new products

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The DIC DI-80 3½-digit panel meter, suitable for OEM production of digital instruments, is housed in a 68 x 28 mm panel cut-out metallic box. It is available in two basic ranges of 200 mV and 1.999 V DC operating at 5 or +5 V for a range of parameters. Also available are DPMs for measuring high DC and AC voltages, ohms and DC/AC current. The DPMs are provided with auto polarity, over-range indication and programmable decimal indication. Other options are also available with facilities such as BCD outputs, Hold condition, use of



For further information contact: Digital Instruments Corporation N.B. Chamber. Opp. Canara Bank, Sajpur Bogha Ahmedabad 382 345

#### SOLDERING IRON

SOLDRON have recently introduced a 50 Watt/230V soldering iron. This new model has features similar to the 25 Watt quick heating light weight solder-ing iron. Due to its high efficiency, the 50 Watt soldering iron can replace the conventional 65 Watt soldering iron and the resulting saving is electrical consumption is claimed to be about 20%. The iron comes with a 'Cool Grip' polypropylene handle. 5mm spade and chisel type slip on bits are available.



For further information, write to Bombay Insulated Cables & Wires Co. 74, Podar Chambers, S.A. Brelvi Road, Bombay 400 001.

DISPOSABLE DISC WITH HOLDER Imexu (India) have developed a disposaknow-how from the Fibral Organisation of UK. The disc is suitable for applications such as cleaning, surface preparation and conditioning, micro-deburring, polishing, decorative finishing, denibbing, and de-fuzzing in a variety of industries-metalwroking, electronics, woodworking, aerospace, automotive, ceramic, metal and non-metal. The 75mm dia disc is made from low density. three dimensional impregnated abrassive with controlled cutting action. It fits the holder without the use of any tool, by positive mechanical locking in a fraction spindle and, as such, can be mounted on any portable/stationary tool. The holder need not be removed from the tool machine for changing the disc. The disc -plain, spirally wound, flap, and puma-and in aluminium oxide and silicon carbide of 80-600 grit. The product comes in kits of 15 discs each with two holders



For further information write to: Imexu (India) 6-A Vaibhav Industrial Estate, Saki Vihar Road, Saki Naka, Bombay 400 072

LIGHTED PUSH BUTTONS

Efficient Engineers have developed a new series of lighted push buttons and indicators with rectangular bezel of 48 x 24 mm. Two independent lamp circuits and one or two pole self cleaning and snap acting microswitches in momentory or maintained action are offered. Any legend can be engraved on the front facia. These push buttons are suitable for process control instrumentation, supervisory remote control systems, data acquisition, control systems, sequencing logic controls, hierarchy controls and turn key instrumentation.



For further information, write to: Sai Electronics Thakore Estate Kurla Kirol Road, Vidyavihar (West) Bombay 400 086

QUARTZ DIGITAL STOP-WATCH

ION Electricals have developed a quartz digital stop-watch, which features the Lap function (useful in sports events), an annuracy of 0.001% at 25 C, and a resolution of 0.01 second (for readings up to 30 minutes). The full range covers hours. The stop-watch has modes of operation—Start/Lap/Stop and Start/Stop. The ergonomically designed case fits perfectly in the palm operation is controlled by two thumbswitches. On the switch being pressed, a Beep sound indicates that the switch has made proper contact. The switches are positioned such that left or right handed operation is equally easy. The stop-watch works on a single penlite cell with a service life of about two years. It measures 6.5 x II x 3 cm and weighs less than 150 g. A silk-strap is provided for safe handling.



For further information write to: ION Electricals Unique House, 5th Floor,, 25 S.A. Brelvi Road, Bombay 400 001

#### 16 PIN IC CLIP

Comtech have introduced their T-16 IC clip for 16 pin DIL packages. It provides easy access to ICs through non-shorting electrical contacts with positive mechanical clamping. Selectively gold plated spring brass contacts are designed for wiping action. The con-tact-comb with its insulating barriers provides easy positioning and prvents accidental shorting of adjecent leads.



For further information, write to: Component Technique 8, Orion Apartments, 29/a, Lallubhai Park Road, Andheri (West), Bombay 400 058.

#### Great little printer

Now available from STC Electronic Services is the Centronics GLP (Great Little Printer) Series dot matrix printer. Completely portable (3 kg), it measures just 333x191x70 mm. features a print speed of 50 c.p.s. (draft printing); near letter quality printing (12 c.p.s.); character pitch of 5, 8.5, 10, and 17 c.p.i.; horizontal tabulation; 48 international characters; high-resolution pin-addressable and IBM PC block graphics.

The GLP offers a choice of enlarged, con densed, emphasized, and double-strike print modes, as well as subscript, superscript, and underlining facilities. In addition, two copies besides the original may be obtained.

To interface the printer with a wide range of commonly used PCs simply requires a connecting cable to a Centronics parallel interface or a single unit incorporating the parallel inter-

face plus the RS232. The GLP's low cost, ease of operation, size and compatibility make it an ideal choice for the personal computer user.

STC Electronic Service Edinburgh Way

Essex CM20 2DE Telephone: (0279) 26811

(3290/1)

(3280/7F)

#### New rotary step switch A new range of versatile rotary miniature step switches, manufactured by Henapot of Switzerland, is now available from Stanler

The HS 24-12 range allows users to choose up

to 12 switching positions and segments: all have a make or break switching option.



Various output pin arrangements are available Maximum switching voltage is 250 V AC or 150 V DC, and the maximum switching current is 0.5 A AC or DC

Stanler Components Limited Rusiness Centre Hay Lane

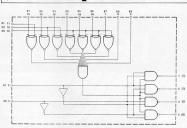
Essex CM7 6ST Telephone: (0376) 40902

#### μP programmable mapping decoder

A new low power ISO-CMOS programmable mapping decoder, the type MV74HCT515 designed for use in high speed memory and peripheral address decoding systems has been announced by Plessey. The device can also be used as a 9-bit pin programmable code

The MV74HCT515 decodes two binary inputs, A<sub>0</sub> and A<sub>1</sub>, to select one of four mutually exclusive, active low outputs (00 to 03), en abled by a pin-programmable 9-input AND gate, E1...E9. Inputs E8 and E9 are permanently active high, while E1...E7 may be programmed active high or active low in any

## new products



MV74HCT515 logic diagram

combination by hard-wiring inputs P1, P2, and and AC models which will sense most Plessey Semiconductors Limited Changy Manny

Swindon SN2 2QW Telephone: (0793) 36251 Tolov: 449637 (3280/10F)

#### Hybrid and stepper motors

Crouzet's new range of hybrid and permanent stepper motors which includes twenty-one types plus controllers in the hybrid servo stepper motor range and five types plus controllers in the nermanent magnet stenner motor range, offers significant advantages over many Also available is an extensive range of multi-

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state output timers with adjustable timing intervals from 60 milliseconds to 10 hours. Each S-range timer is designed to operate at any voltage between 10 and 264 V. Crouzet Limited

(3280/4F)

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#### Capacitive proximity sensors

### Following the recent introduction of inductive

proximity sensors and photoelectric devices, Sigma has now added a new range of capacitive proximity sensors to its already extensive switch and sensor range. The new capacitive range contains both DC

materials including liquids. DC models are tubular with either plain all-

plastic bodies, or threaded metal-caseo bodies. The shielded models have a sensing range of 15 mm, unshielded ones 20 mm, Outputs are p-n-p or n-p-n, normally open, with a maximum output current of 200 mA.



A similar range of sensors is available for AC input. The shielded and unshielded models have sensing ranges of 15 mm and 20 mm respectively, while a flat disc model offers an extended sensing range of 70 mm. AC sensors have NC or NO output at 5...200 mA. Siama Limitea

Spring Road Herts SG6 4AJ Telephone: (04626) 3841 Telex: 825535

(2290/11E)

#### Single-chip TV control and tuning IC

The new SAA1280 from ITT Semiconductors provides push-button synthesized tuning of up to 16 stations, and analogue volume, brightness, contrast, and saturation confrols. The chip features three bandswitch and one VCR switch outputs, four analogue outputs, four-way multi-standard switching, output for a 1½ digit station display, muting during change of station or band, and tuning voltage generation with rate multiplier.

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## corrections

### QL RAM

#### extension (Aug/Sept 1985 -

both extensions.

P. 54)
In the text (penultimate paragraph) it is erroneously stated that in the 64 K extension pin 11 instead of pin 13 of IC<sub>5</sub> must be used as CS. In fact, pin 11 must be used in

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